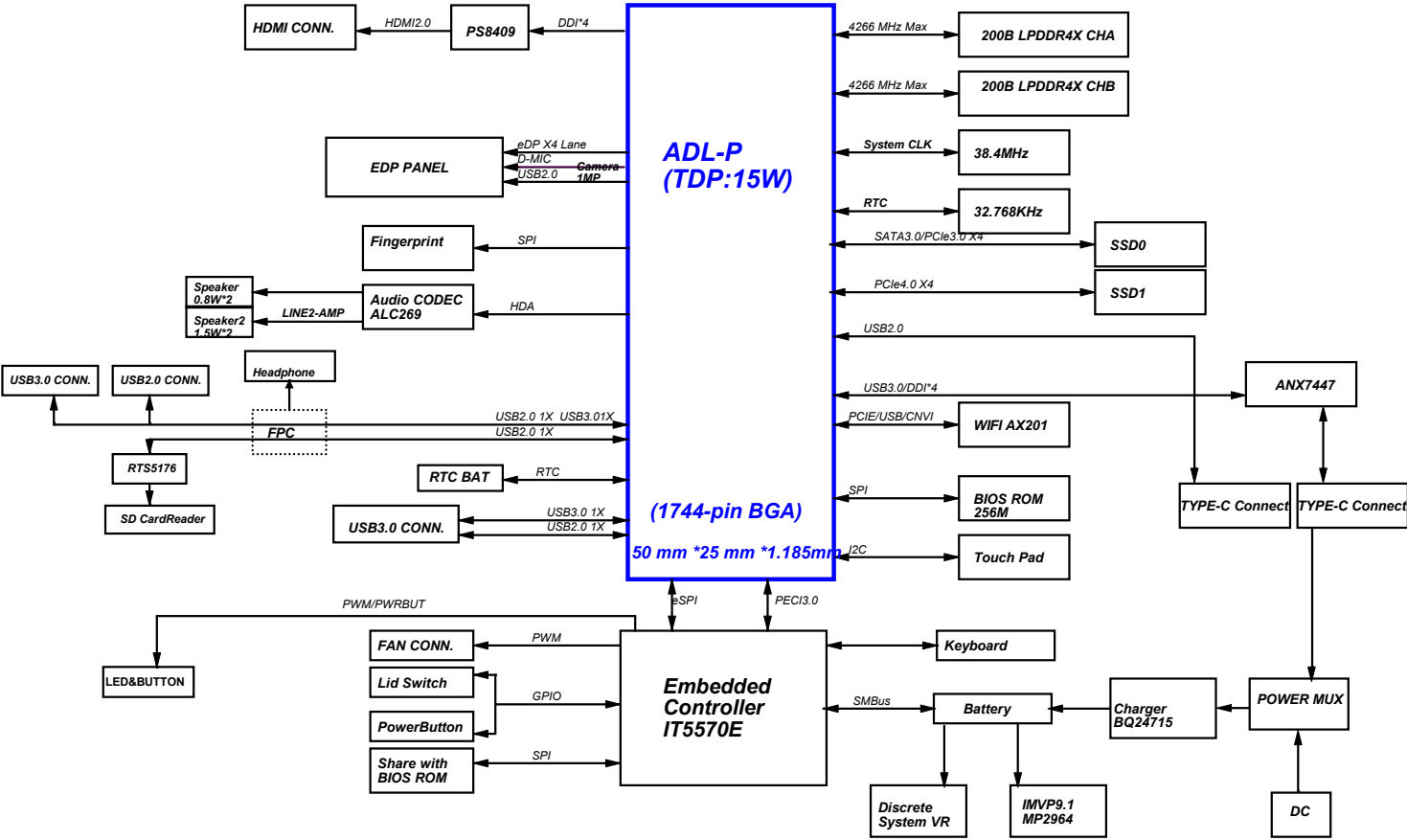


# Block Diagram




Doc No.		 亿成电子		
Designer				
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Approved				
Title:				
DO NOT REPRODUCE	Scale	SCHEMATIC	Drawing No.	Rev. V1.0
FOR INTERNAL USE ONLY	Date:	Issued December 13, 2002	Sheet 01 of 05	

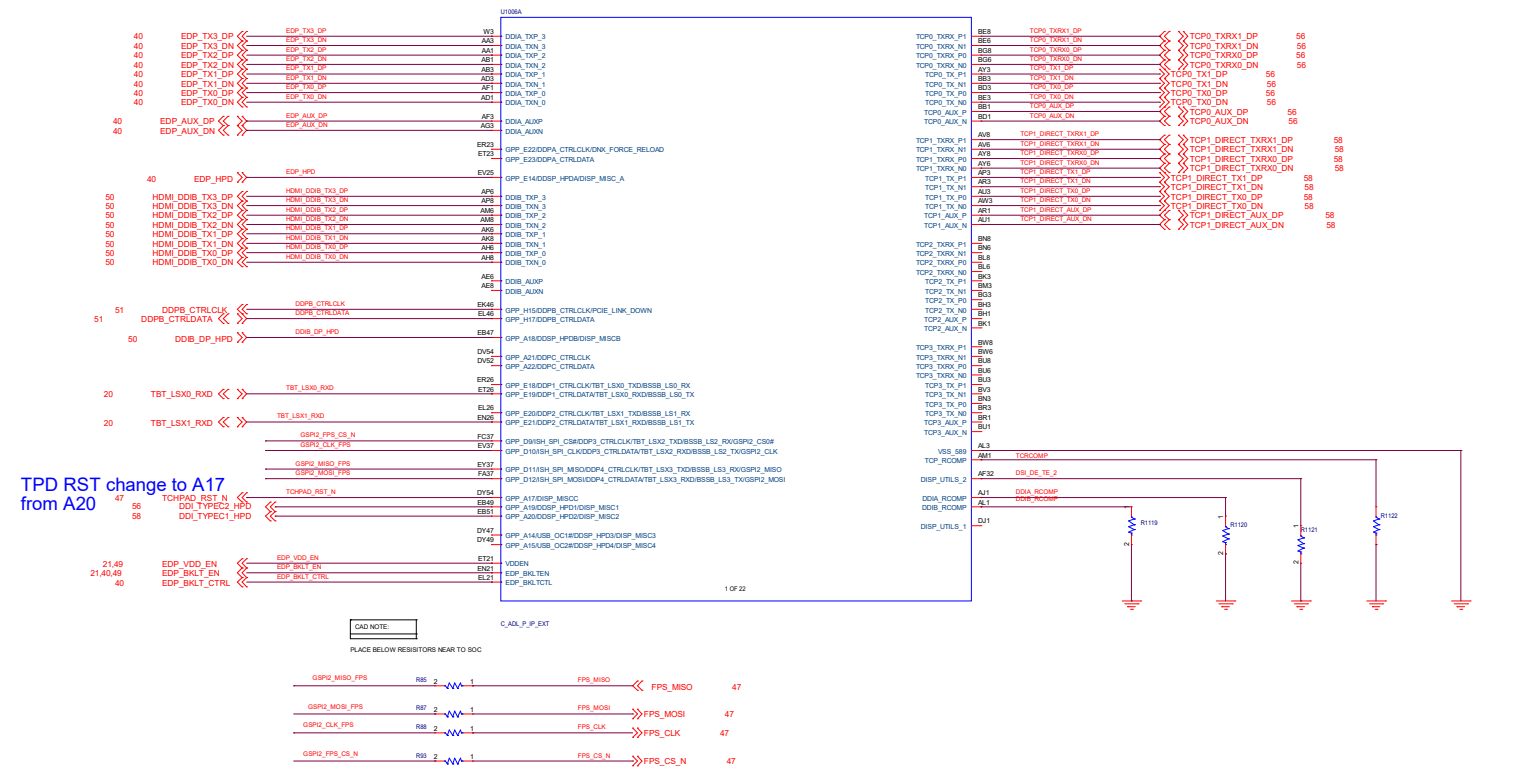
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[illegible]

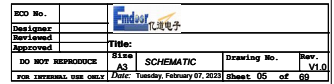
ECO No.		 亿道电子		
Designer				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	Size A3	SCHEMATIC	Drawing No.	Rev. V1.0
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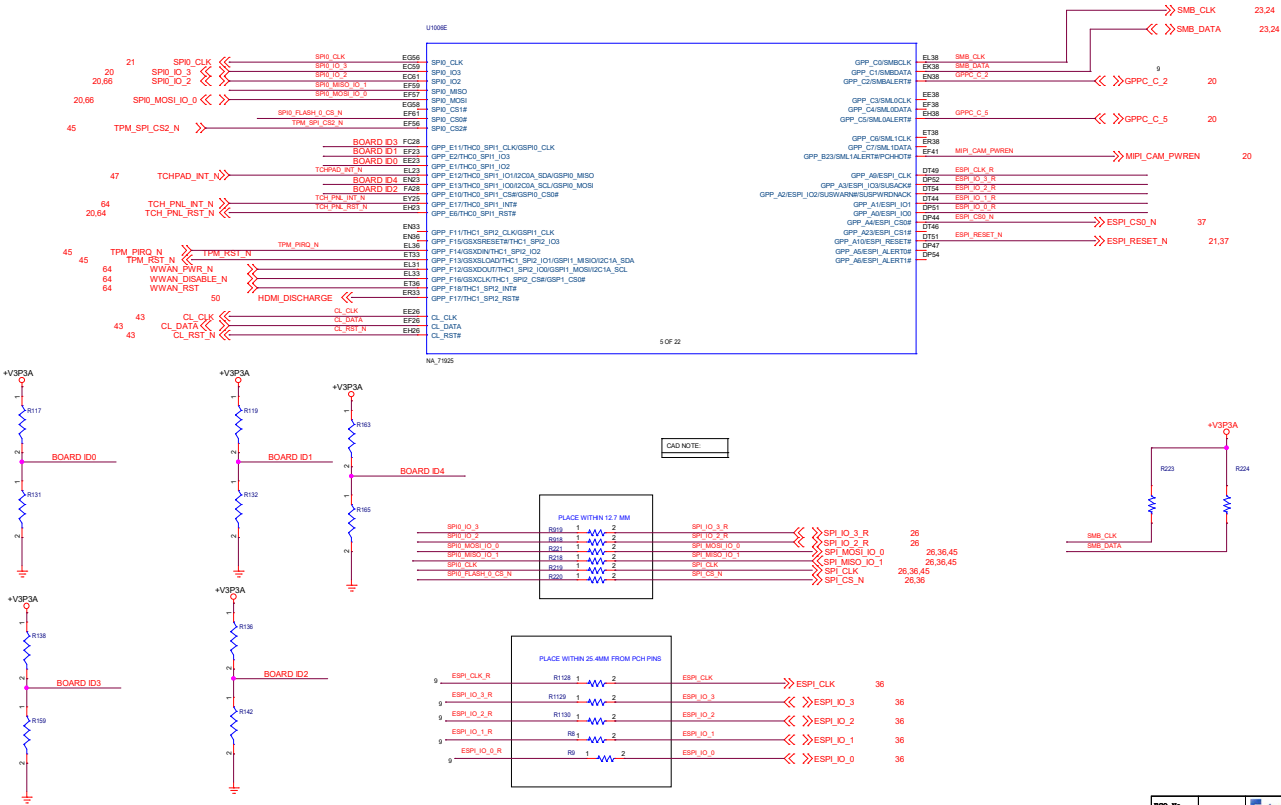


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Rev		Findit
Approved		Findit
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ECO No.		 亿道电子		
Designer				
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Approved				
		Title:		
DO NOT REPRODUCE	Size	SCHEMATIC	Drawing No.	Rev.
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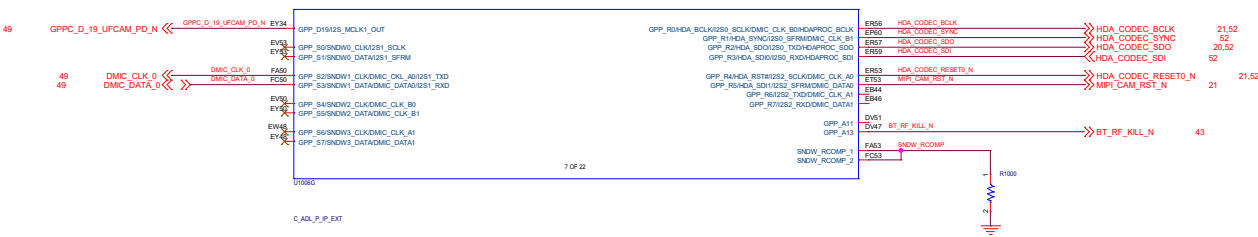


ECO No.			
Designer			
Reviewed			
Approved			
DO NOT REPRODUCED FOR INTERNAL USE ONLY	Rev. 1.0	SCHEMATIC	Rev. V1.0
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## ADL-P (6 OF 22) - LPSS - GSPI,UART/I2C,ISH-SPI,ISH-UART,ISH-I2C/GPIO




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Approved	Title:			
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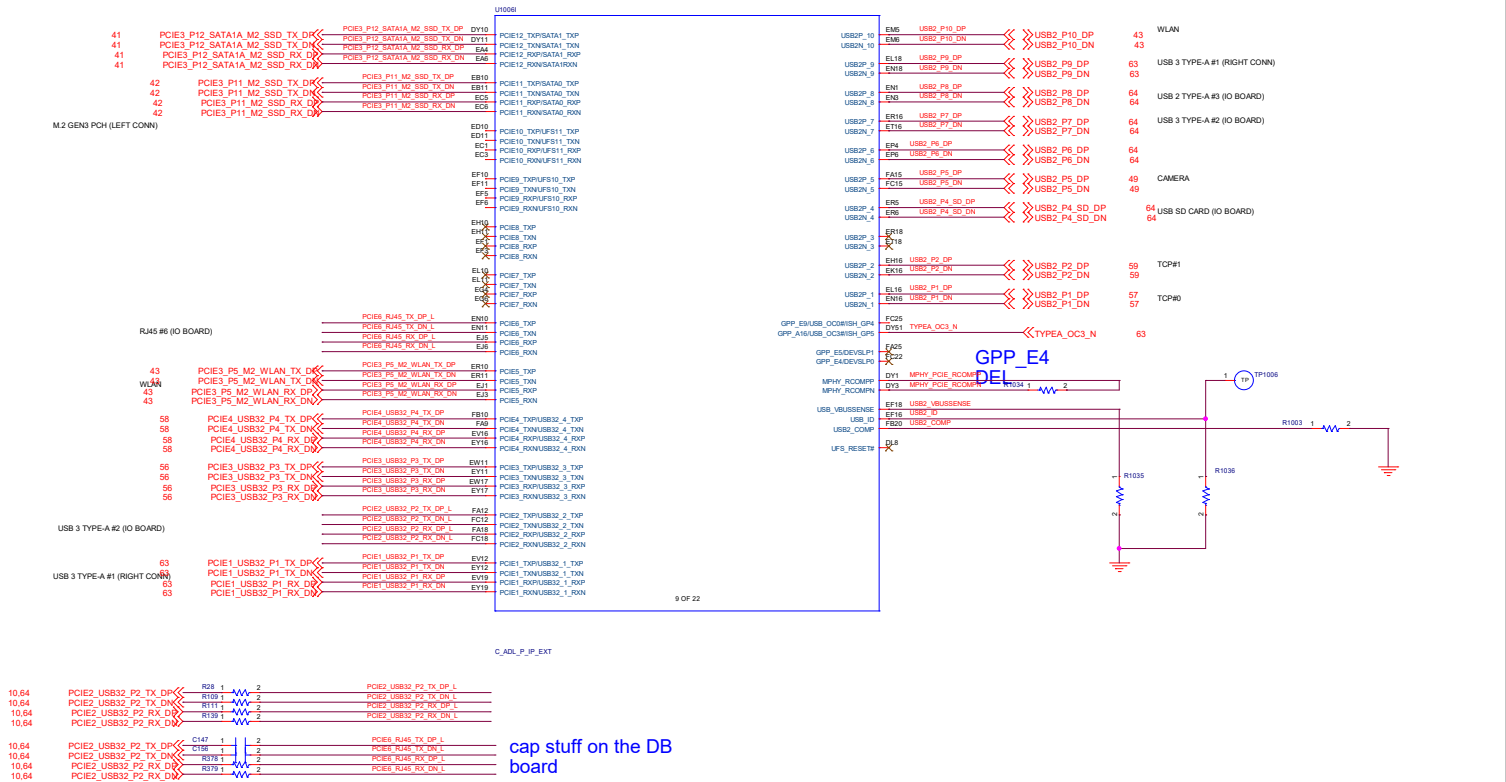
ECO No.			
Designer		Title:	
Reviewed		Size	Rev.
Approved		Unit	V1.0
DO NOT REPRODUCE	1/100	SCHMATIC	Drawing No.
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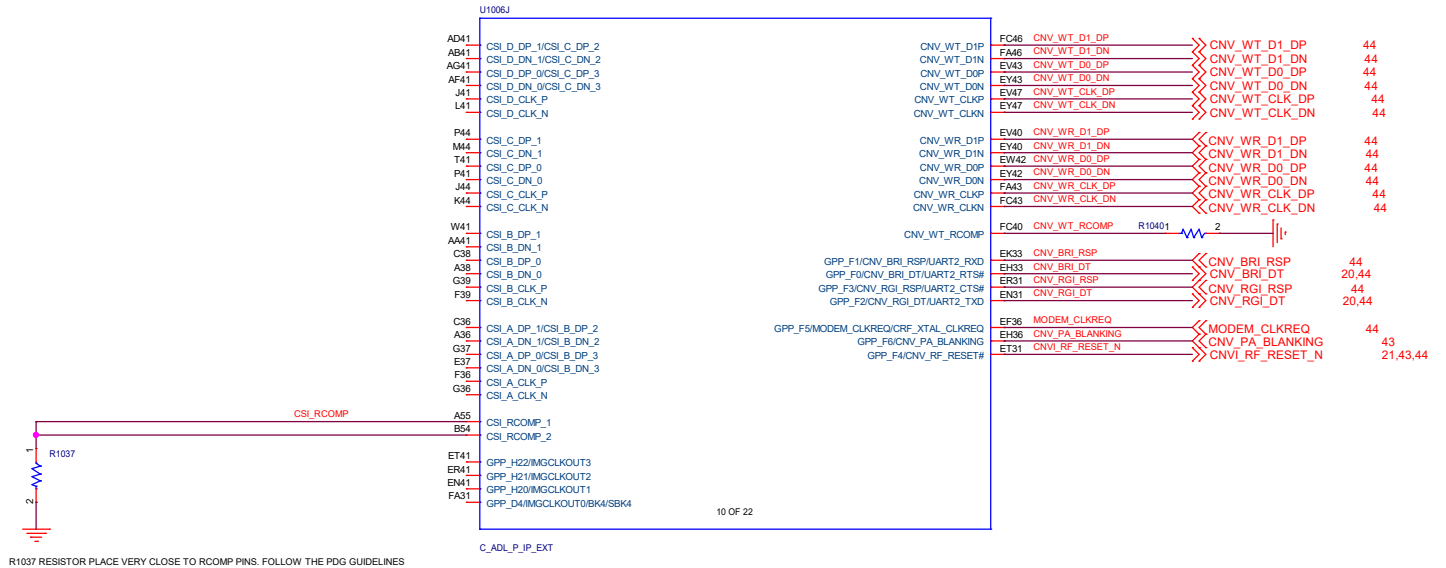



ECO No.		 德化电子		
Designer				
Reviewed				
Approved				
		Title:		
Size	A3	SCHEMATIC	Drawing No.	Rev. V1.0
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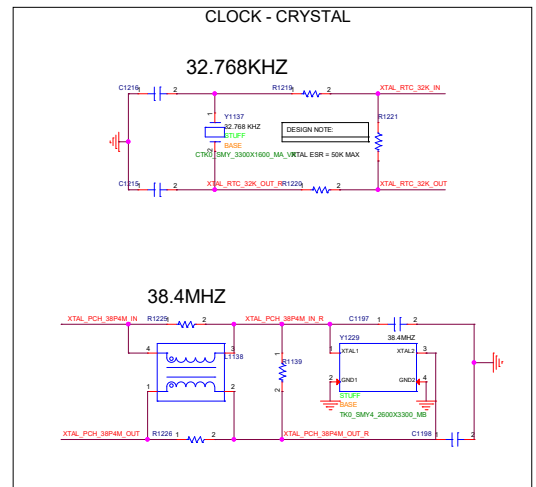
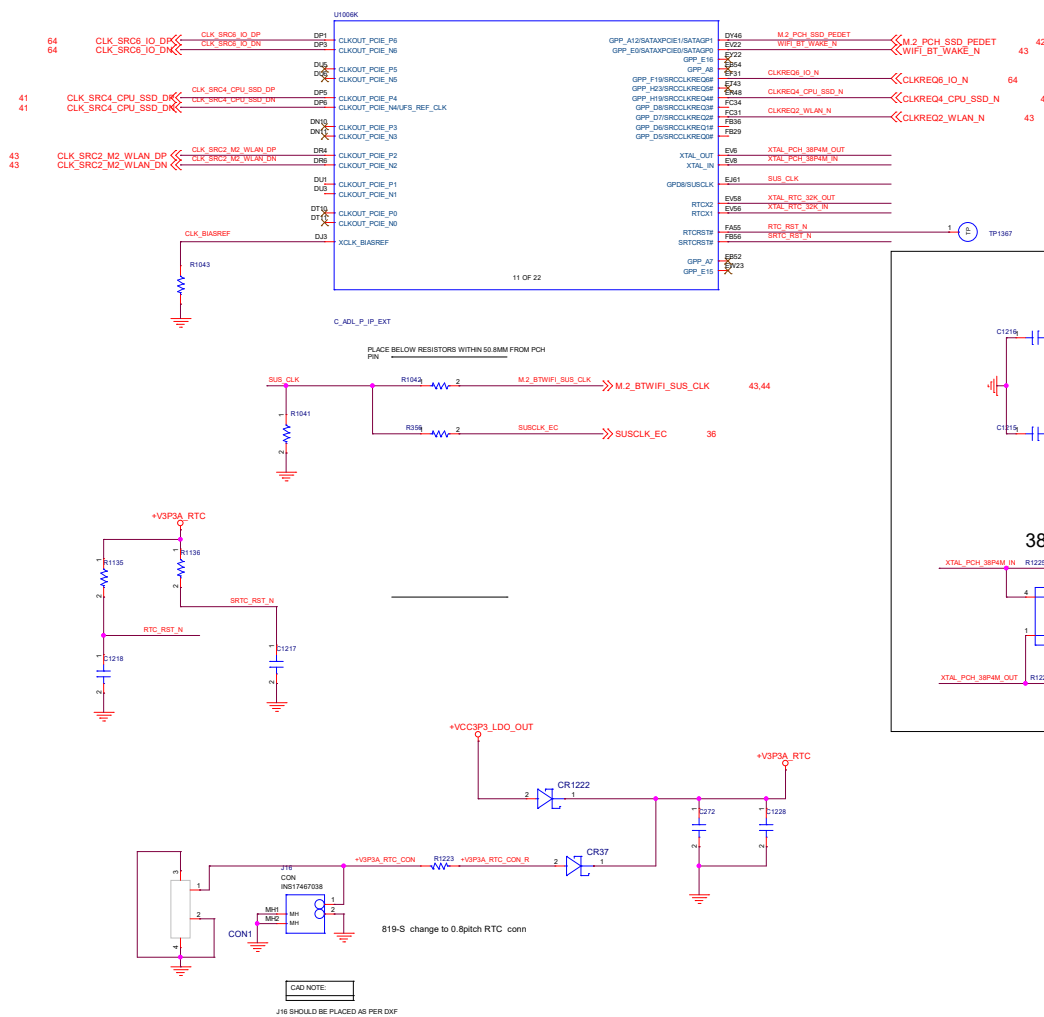
## ADL-P (9 OF 22) - MODPHY




ECO No.				
Designer				
Reviewed				
Approved	Title:			
DO NOT REPRODUCE	Size A3	SCHEMATIC	Drawing No.	Rev. V1.0
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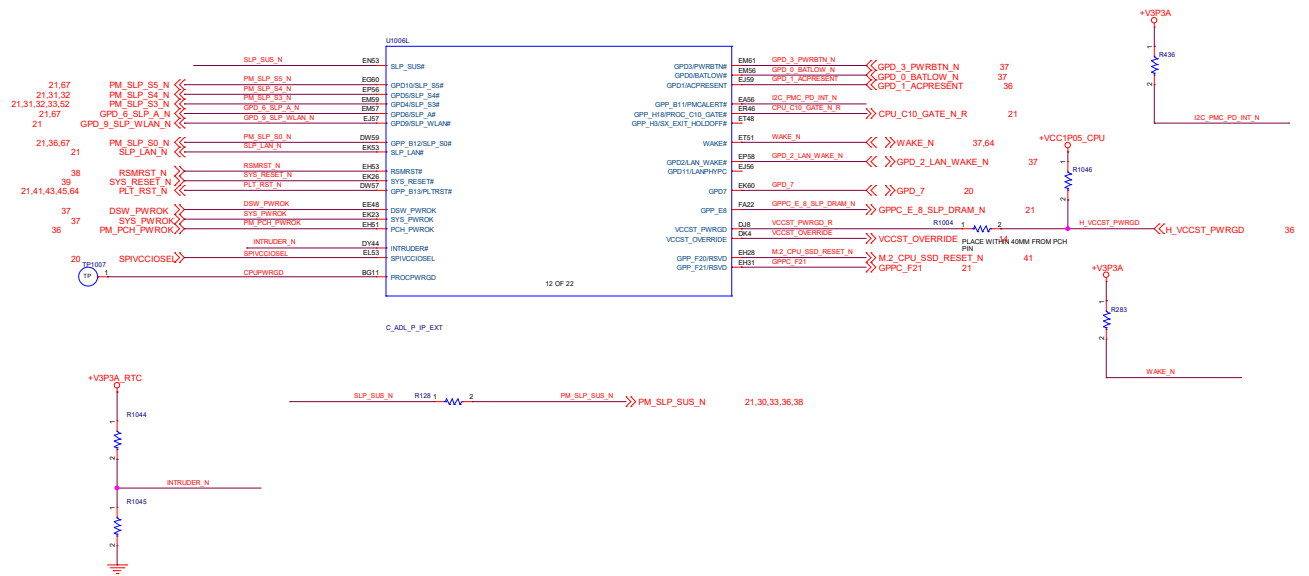



ECO No.				
Designer				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	Size	A4	SCHEMATIC	Drawing No.
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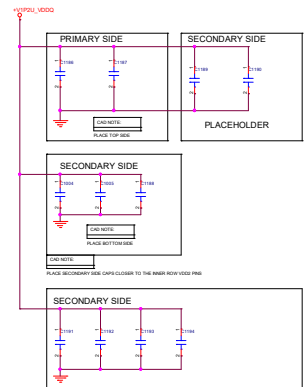
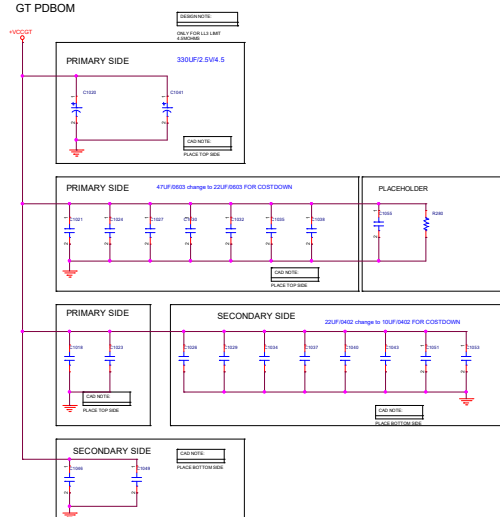
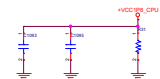


ECO No.		 代通电子		
Designer				
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		<b>Title:</b>		
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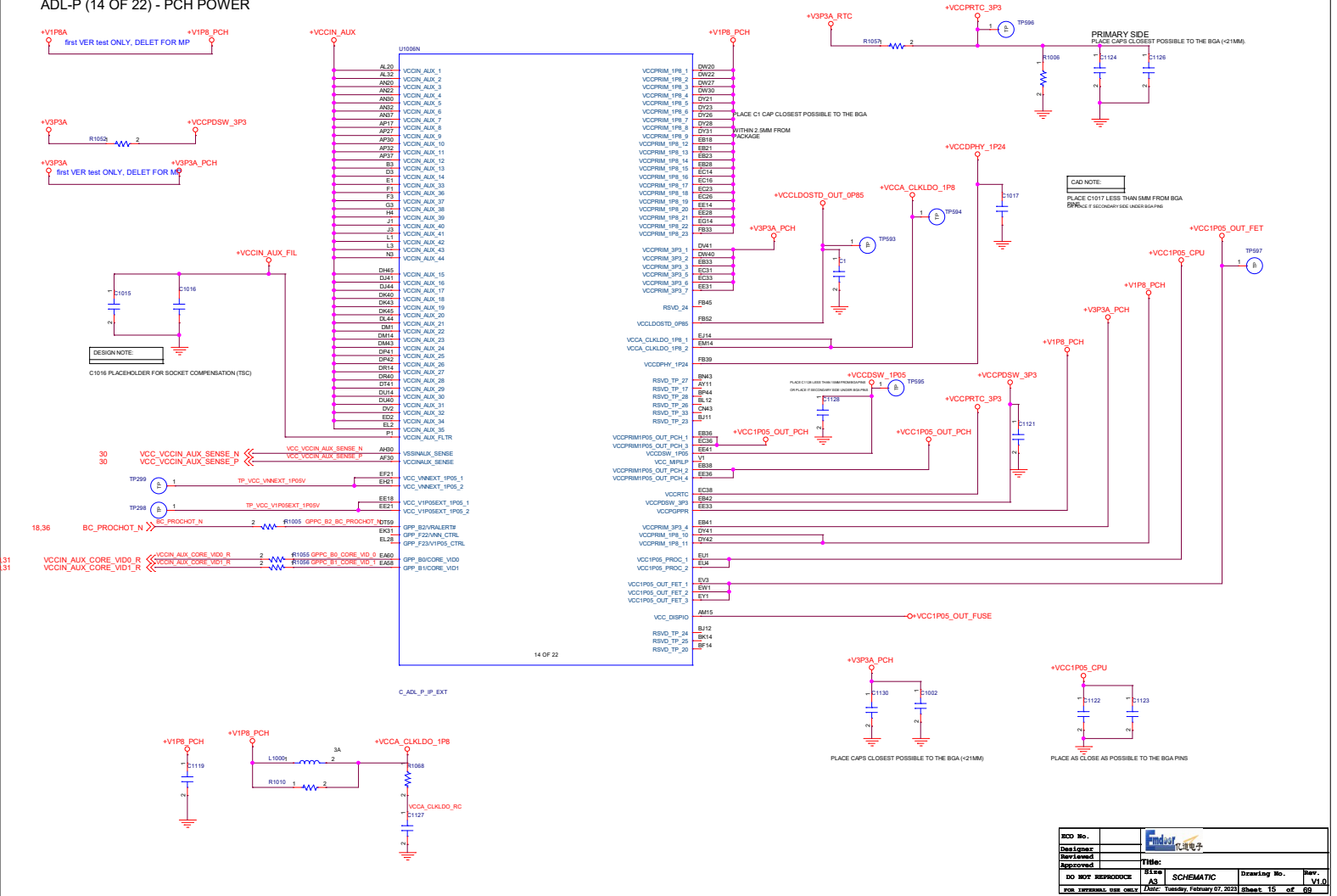



ECO No.		 代理电子			
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DO NOT REPRODUCE		Size A3	SCHEMATIC	Drawing No.	Rev. V1.0
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ECO No.		 尔达电子		
Designer				
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## ADL-P (14 OF 22) - PCH POWER

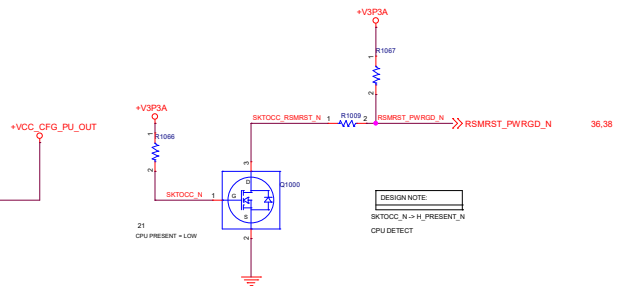
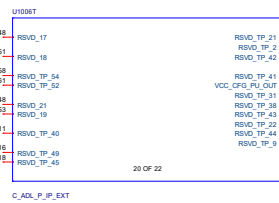



ECO No.	 沁达电子		
Designer			
Reviewed			
Approved	<b>Title:</b>		
DO NOT REPRODUCE	Size	SCHEMATIC	Drawing No.
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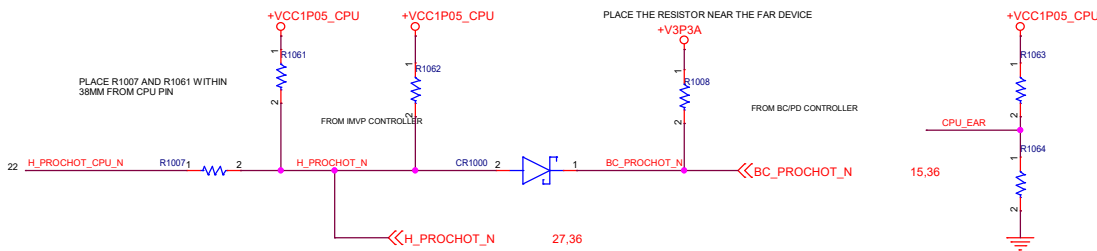
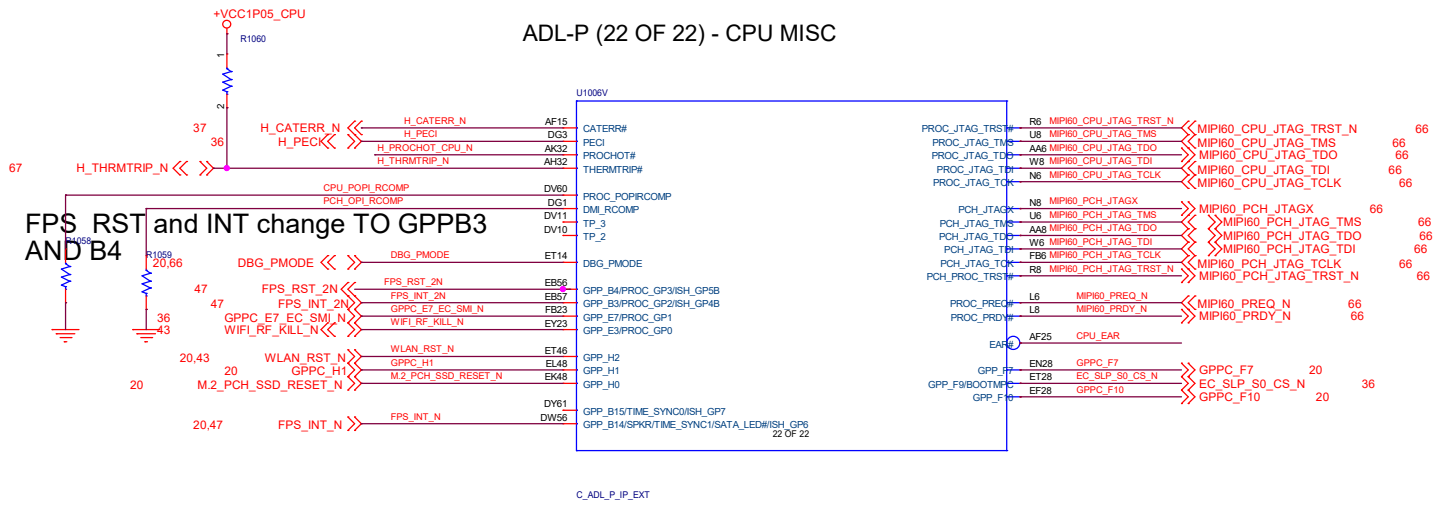
ECC No.			
Designer			
Reviewed			
Approved	<b>Title:</b>		
DO NOT REPRODUCE	<b>Size</b> A3	<b>SCHEMATIC</b>	<b>Drawing No.</b> 
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ECO No.		 艾迪电子			
Designer					
Reviewed					
Approved			Title:		
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## ADL-P (22 OF 22) - CPU MISC

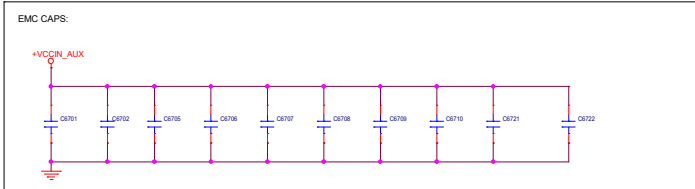
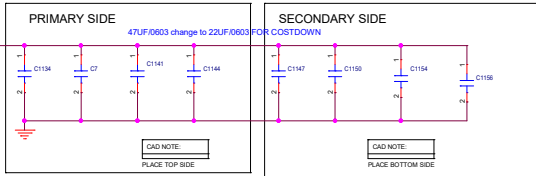
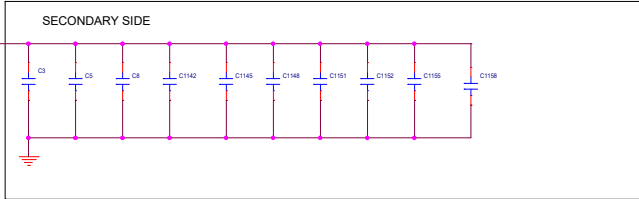
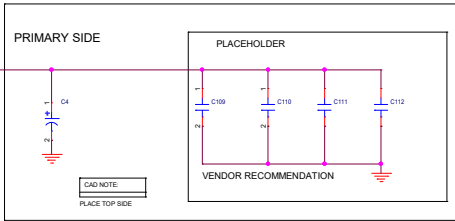


ECO No.				
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Approved				
		<b>Title:</b>		
<b>DO NOT REPRODUCE</b>		<b>Size A4</b>	<b>SCHEMATIC</b>	<b>Drawing No.</b>
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+VCCIN\_AUX

THE PD BOM IS FOR 800KHZ SWITCHING FREQUENCY

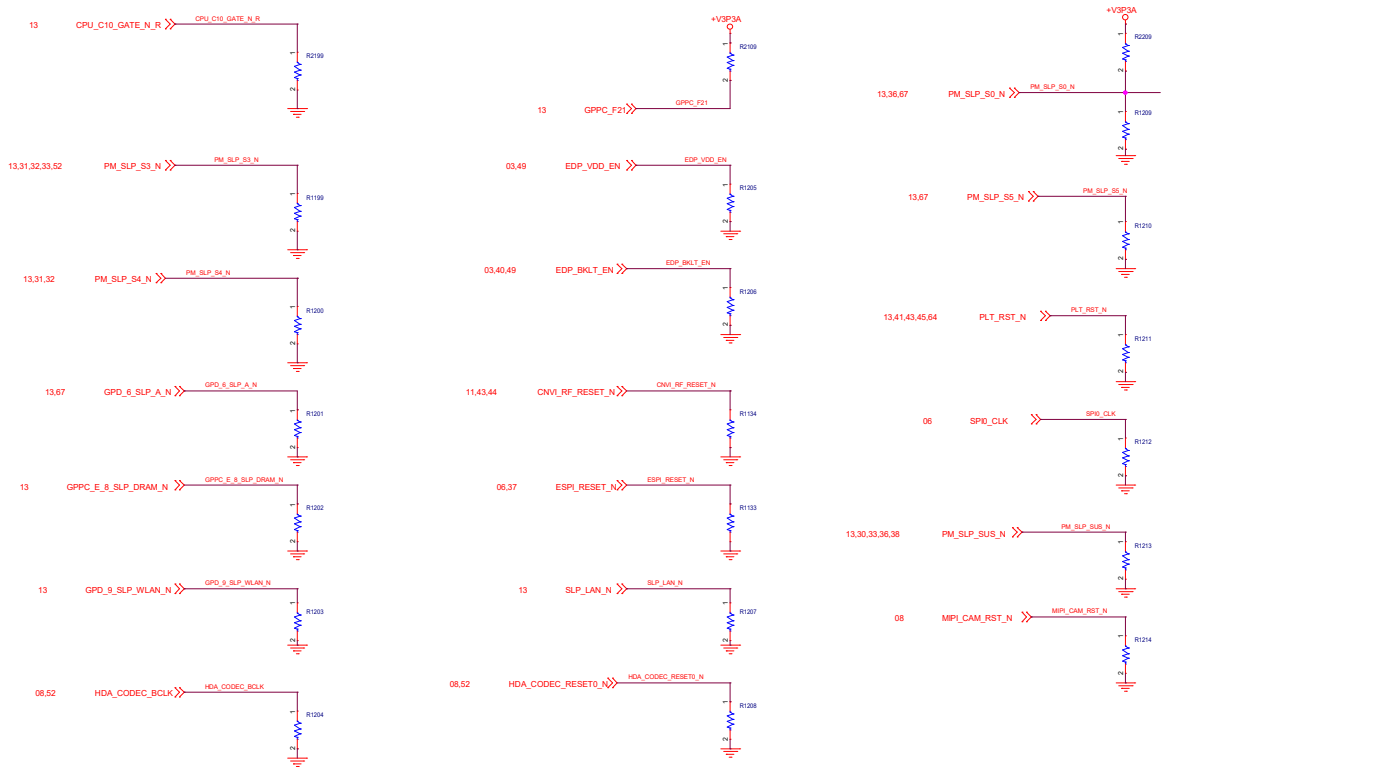
VCCIN\_AUX PD BOM



ECO No.			
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Approved			
DATE:			
DO NOT REPRODUCE	SL&A	Drawing No.	Rev.
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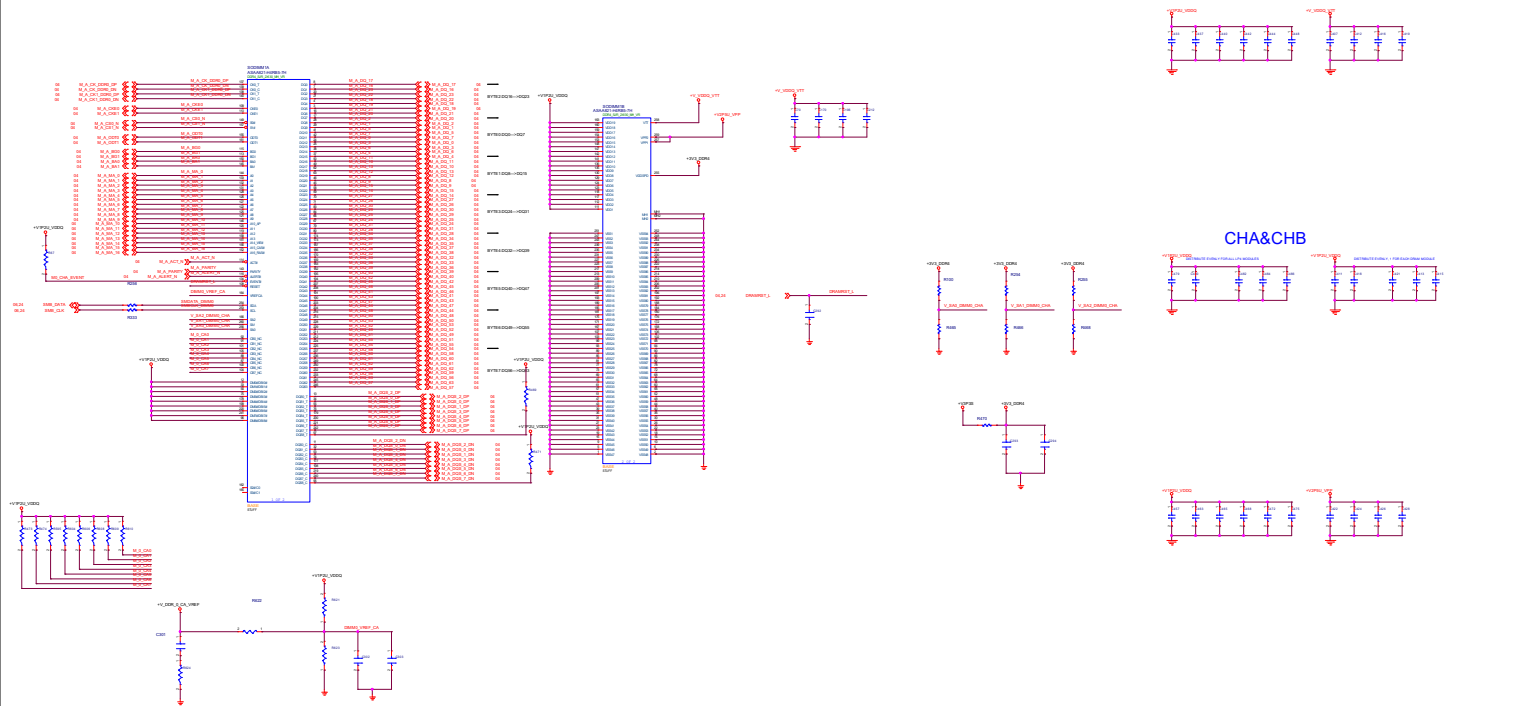


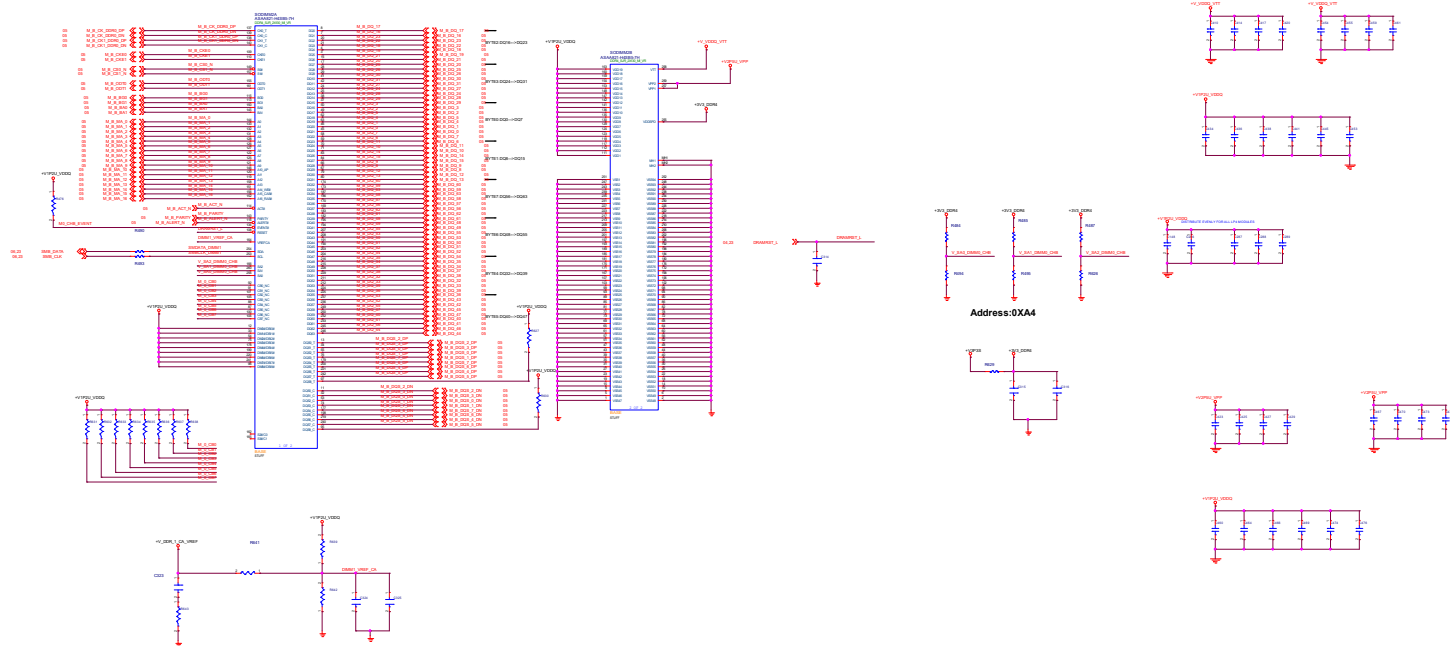
PCH GLITCH ISSUE MITIGATION



ECO No.			
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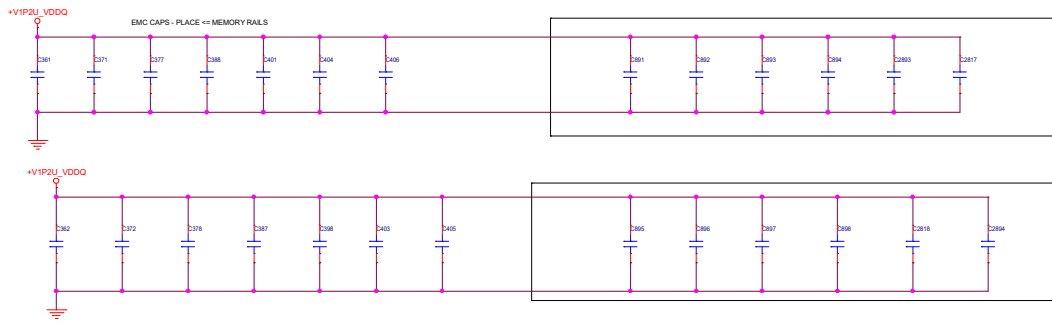





Rev. No.	1.0
Rev. Date	2023-10-10
Rev. Description	Initial Release
Rev. Author	John Doe
Rev. Reviewer	Jane Smith
Rev. Approved	Yes

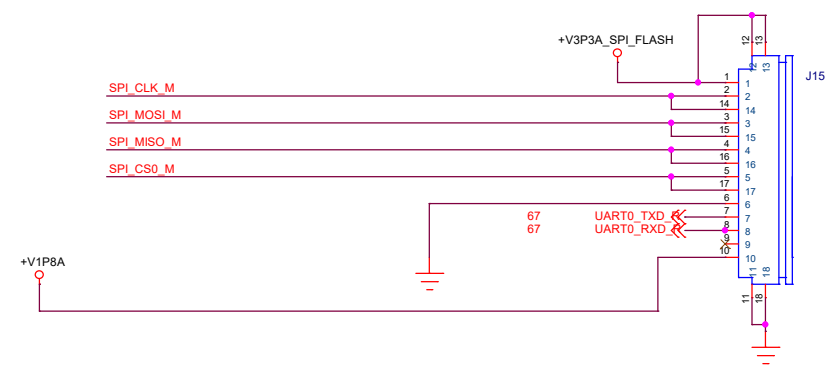
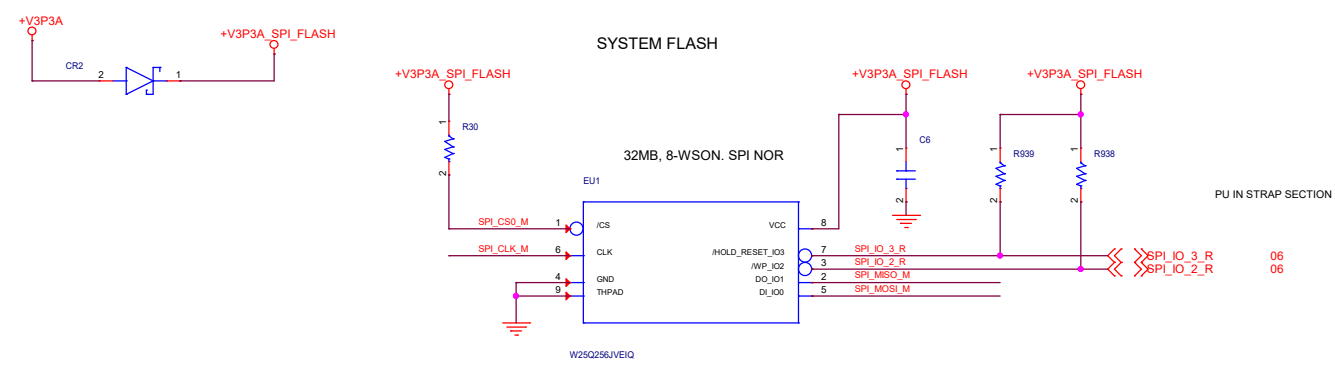


MEMORY - EMC CAPS



CAD NOTE:

ECO No.		 凡道电子
Designer		
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Title:		
DO NOT REPRODUCE	Size Custom	SCHEMATIC
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Designer		Title:		
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Approved		Drawing No. Rev. V1.0		
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**MANUAL BOM UPDATE:**  
SKU-A- M37603-001, MP2964GQKT-2000  
SKU-B- M37604-001, MP2964GQKT-2001

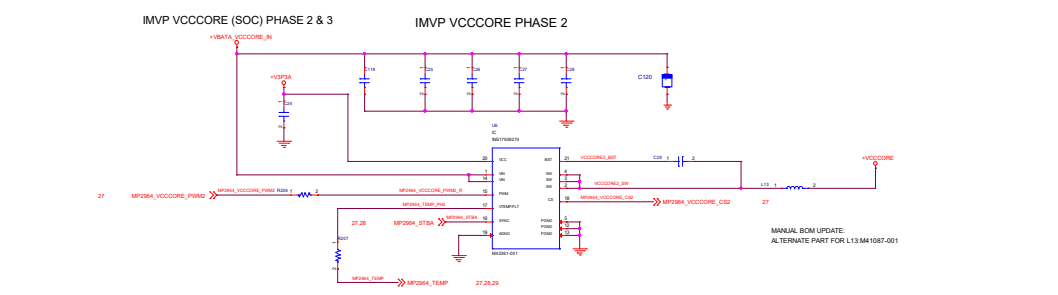
**MANUAL BOM UPDATE:**  
SKU-B: R199- C48657-295 (39K)  
SKU-B: C16- D70538-002 (470PF)

**MANUAL BOM UPDATE:**  
SKU-B: R200- J33770-001 (66.5K)  
SKU-B: C17- M22594-001 (270PF)

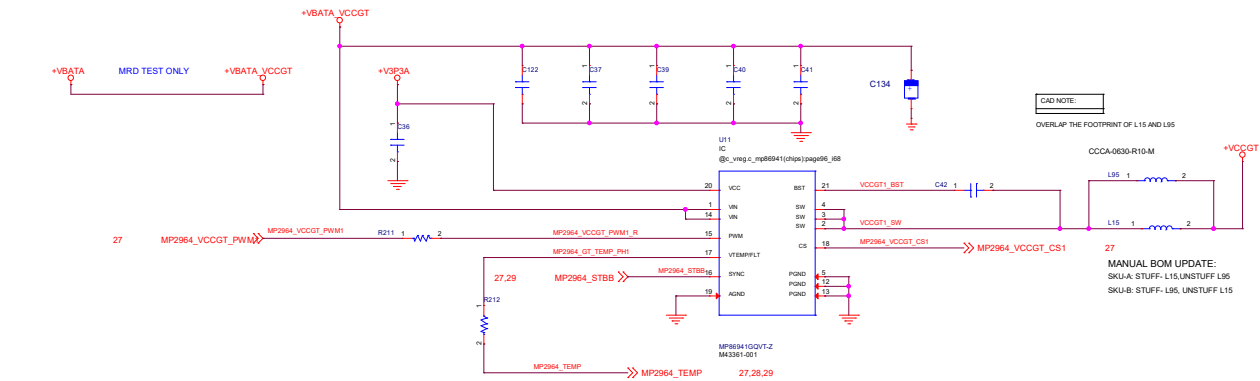
**CAD NOTE:**  
PLACE R12,R123,R126 NEAR EU3

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Approved		Date	Tuesday, February 07, 2023
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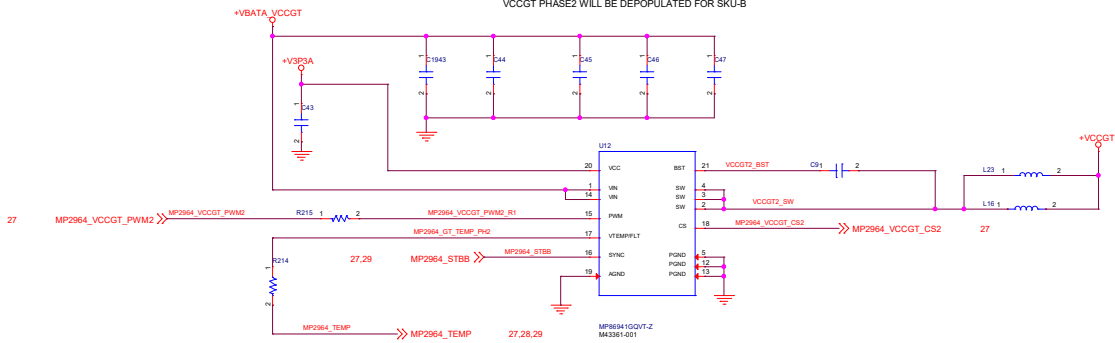
ECO No.				
Designer				
Reviewed				
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DO NOT REPRODUCE	SIZE A2	SCHEMATIC	Drawing No.	Rev. V1.0
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VCCGT PHASE2

IMVP VCCGT PHASE 2


VCCGT PHASE2 WILL BE DEPOPULATED FOR SKU-B



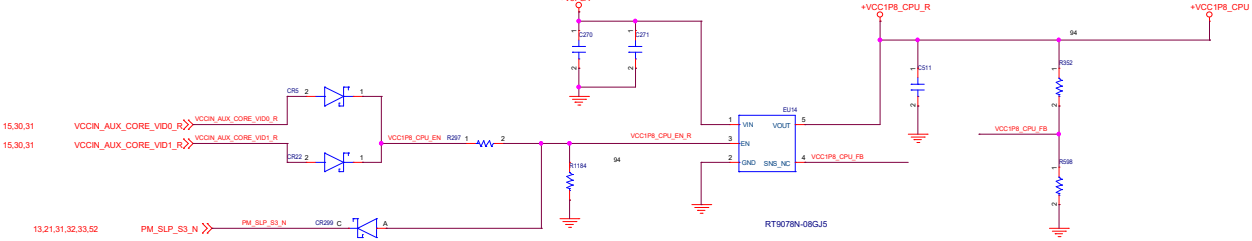
ECO No.		Freescale 机通电子	
Designer			
Reviewed			
Approved			
DO NOT REPRODUCE	A3	SCHEMATIC	Rev. V1.0
eco_systems_soc_dept	JHU	Tuesday, February 07, 2023	Sheet 29 of 69

## VCCIN\_AUX RAIL POWER CONVERSION

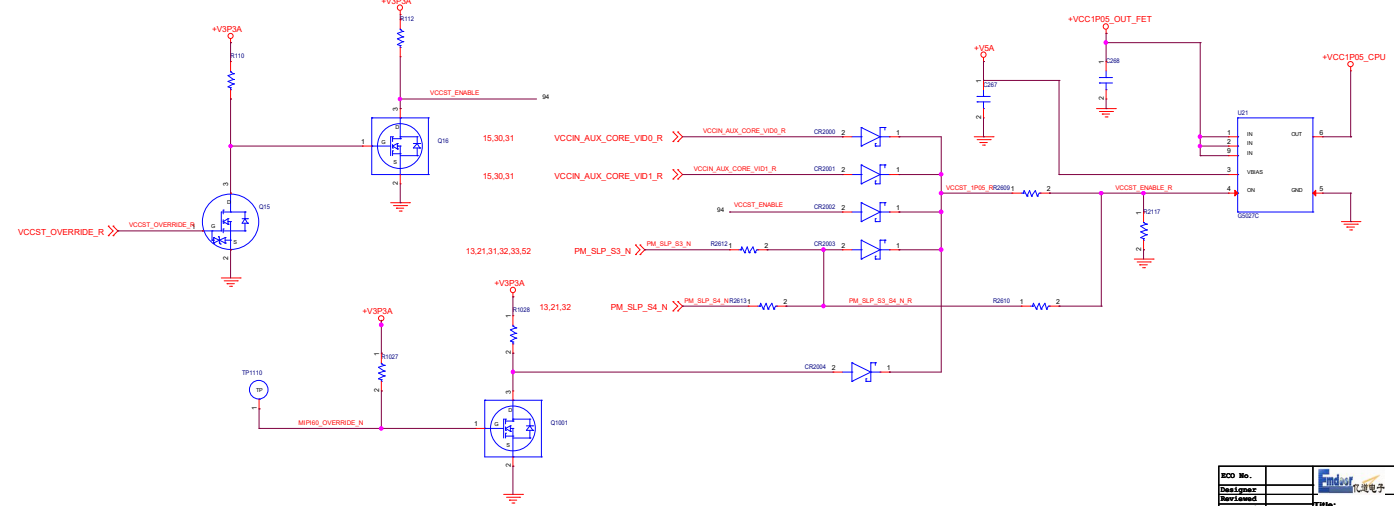


ECO No.				
Designer				
Reviewed				
Approved				
		Title:		
DO NOT REPRODUCE	Size A3	SCHEMATIC	Drawing No.	Rev. V1.0
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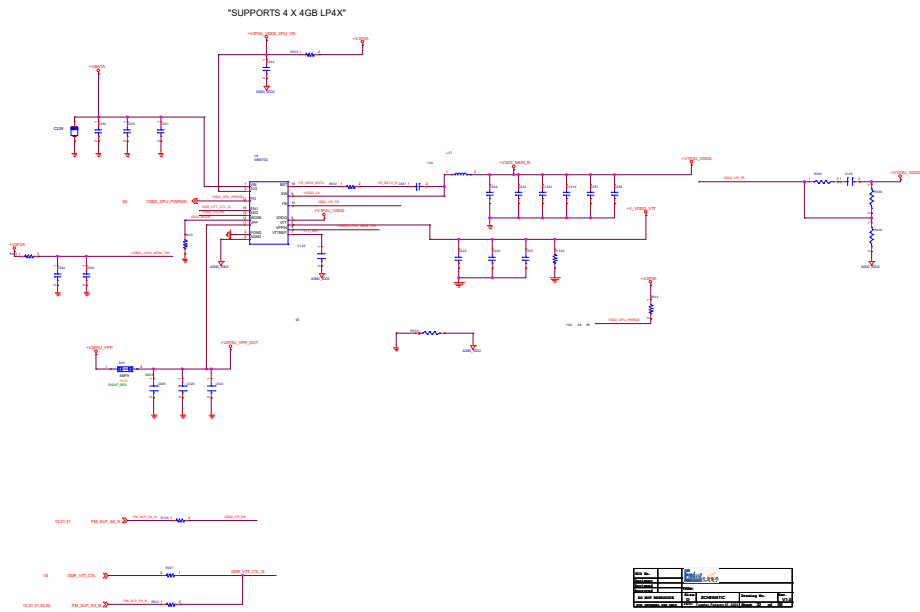
### +VCC1P8\_CPU RAIL GENERATION



### V1.05U\_CPU GENERATION



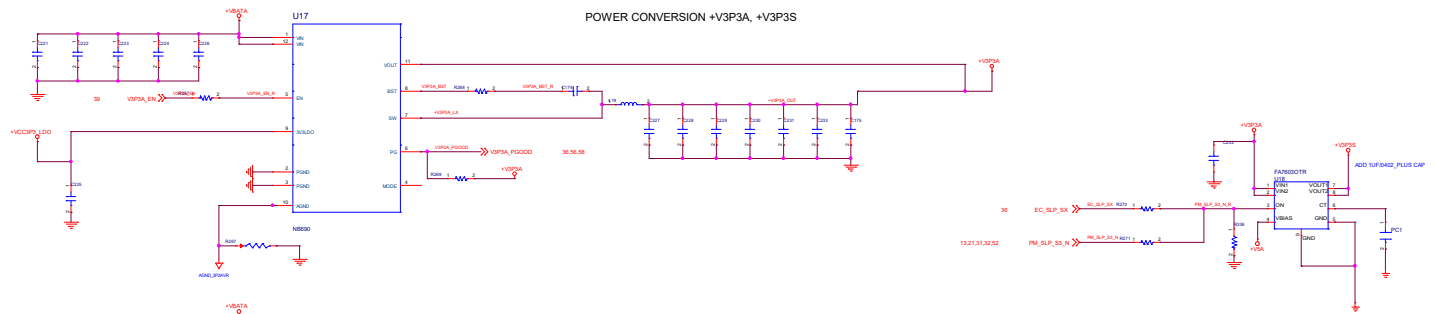
DOC No.		
Designer		
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Approved		
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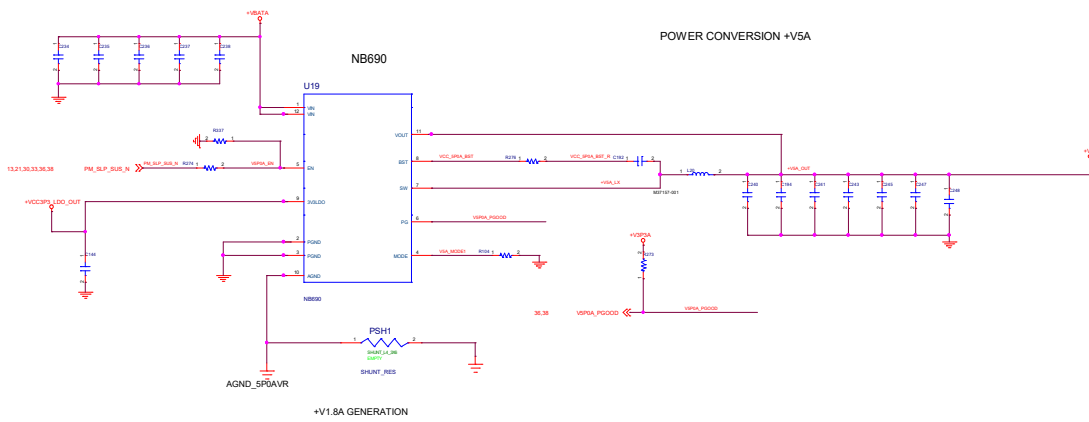


NB690

POWER CONVERSION +V3P3A, +V3P3S

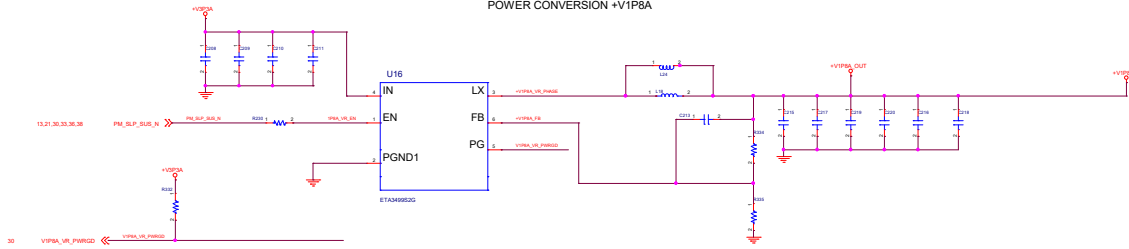


POWER CONVERSION +V5A



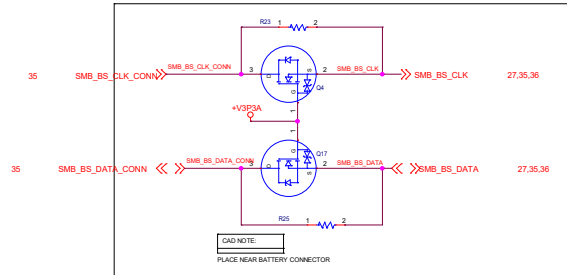
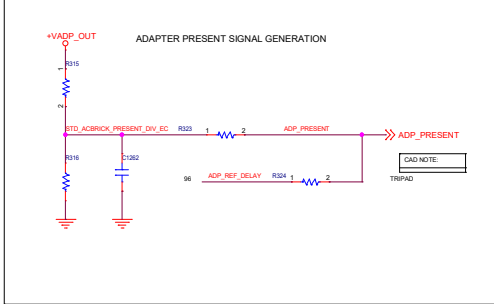
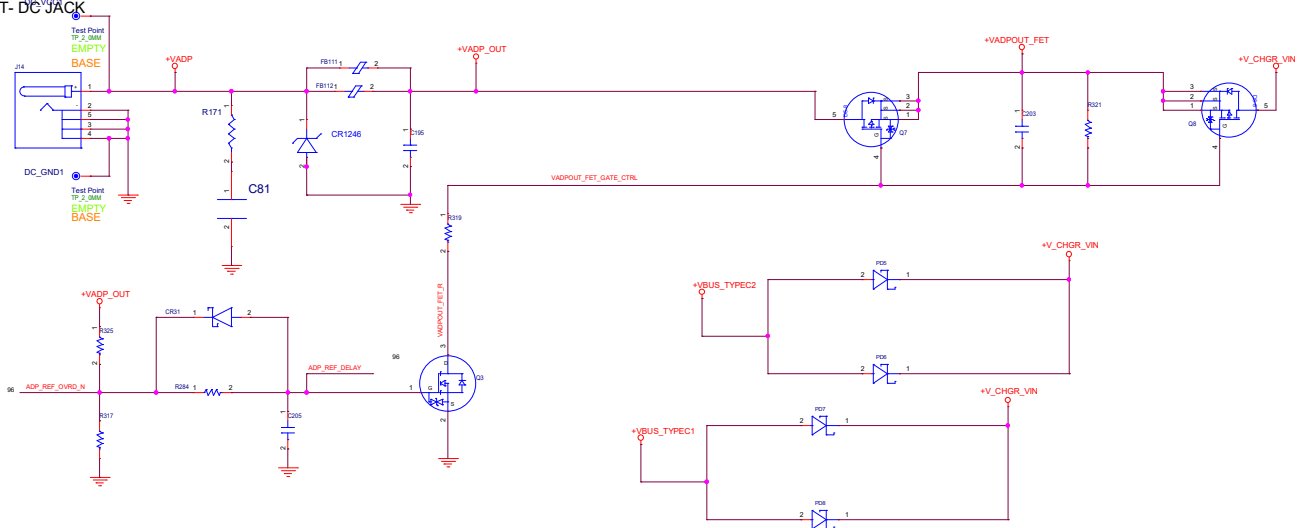
+V1.8A GENERATION

POWER CONVERSION +V1P8A

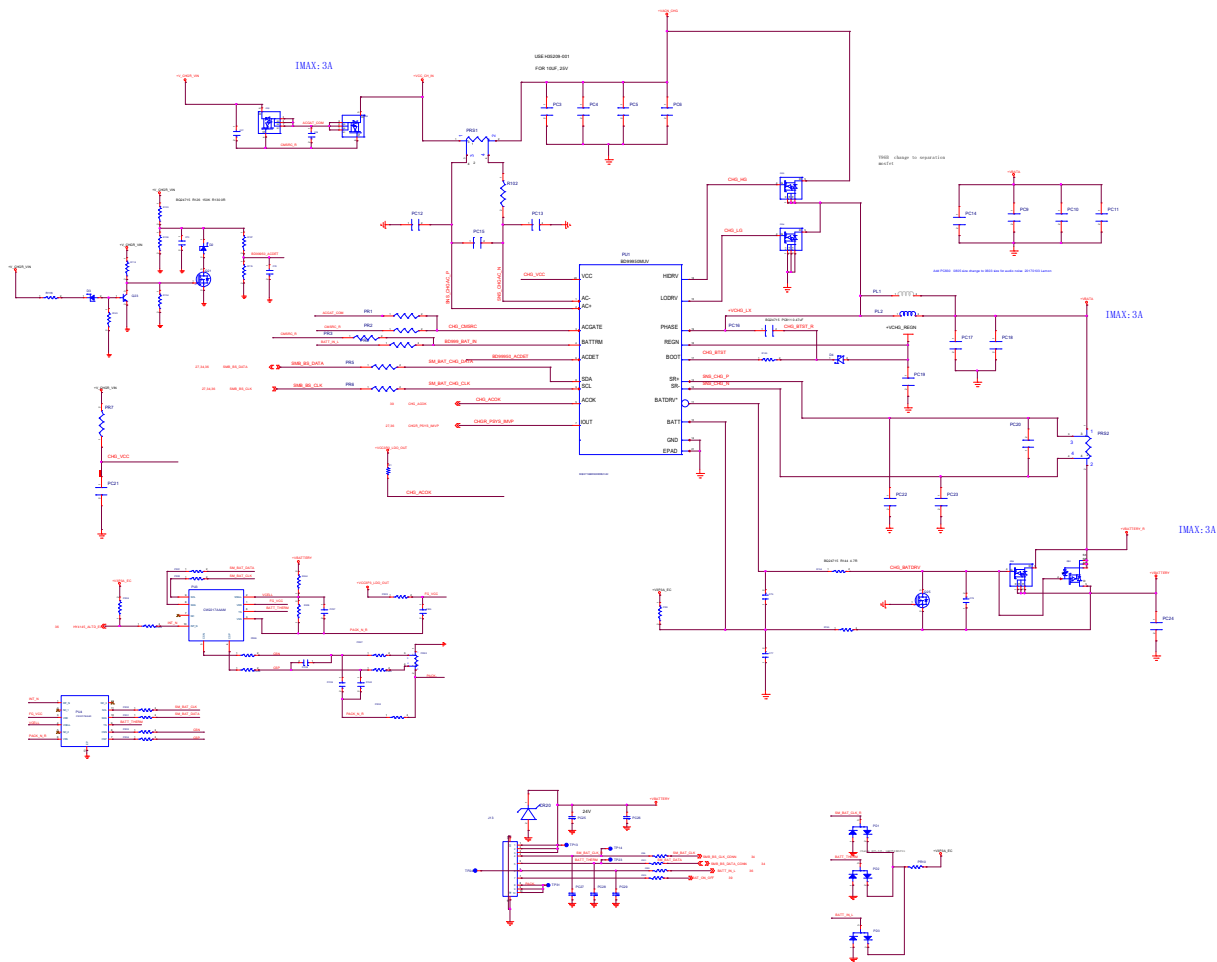


Rev. No.	1	Rev. No.	1
Rev. Date	2023-01-10	Rev. Date	2023-01-10
Rev. Description	Initial Release	Rev. Description	Initial Release
Rev. Author	AD	Rev. Author	AD
Rev. Checker	AD	Rev. Checker	AD
Rev. Approver	AD	Rev. Approver	AD
Rev. Release	AD	Rev. Release	AD
Rev. Status	Released	Rev. Status	Released
Rev. Version	1.0	Rev. Version	1.0
Rev. Project	AD	Rev. Project	AD
Rev. Part	AD	Rev. Part	AD
Rev. Drawing No.	AD	Rev. Drawing No.	AD
Rev. Rev.	AD	Rev. Rev.	AD

POWER INPUT- DC JACK

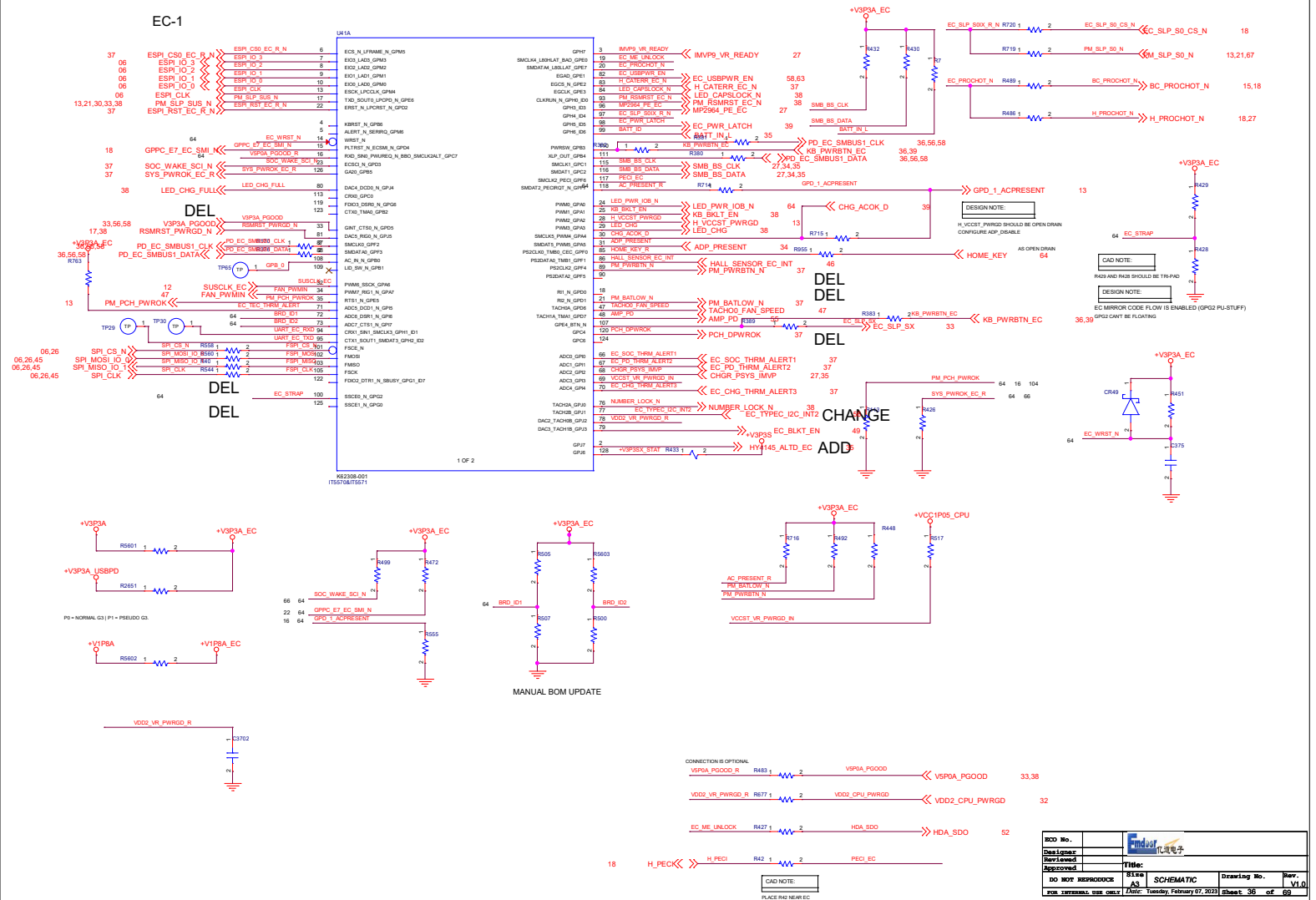


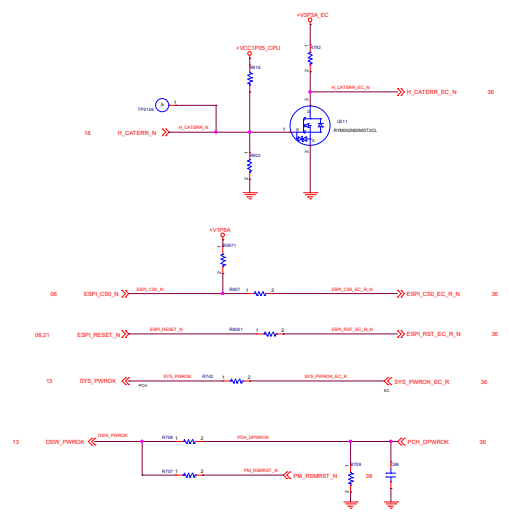
ECO No.				
Designer				
Reviewed				
Approved				
		<b>Title:</b>		
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1.0	John	John	2014-01-01
1.1	John	John	2014-01-01
1.2	John	John	2014-01-01
1.3	John	John	2014-01-01
1.4	John	John	2014-01-01
1.5	John	John	2014-01-01
1.6	John	John	2014-01-01
1.7	John	John	2014-01-01
1.8	John	John	2014-01-01
1.9	John	John	2014-01-01
2.0	John	John	2014-01-01

## EC-1




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ECO No.		 代通电子		
Designer				
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		Title:		
DO NOT REPRODUCE	SIZE A2	SCHEMATIC	Drawing No.	Rev. V1.0
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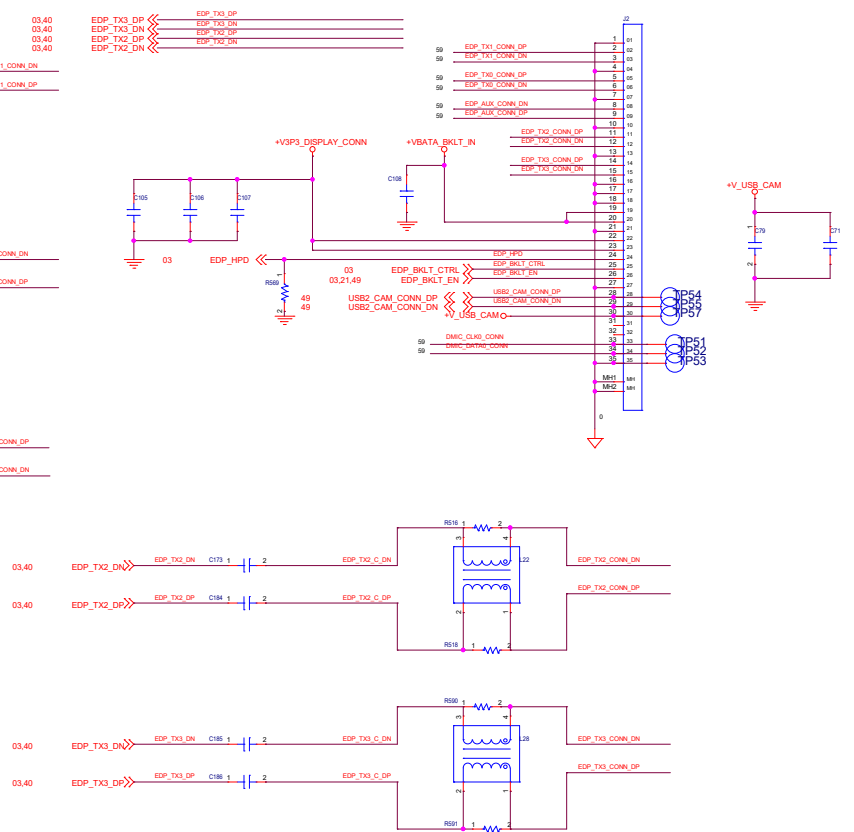
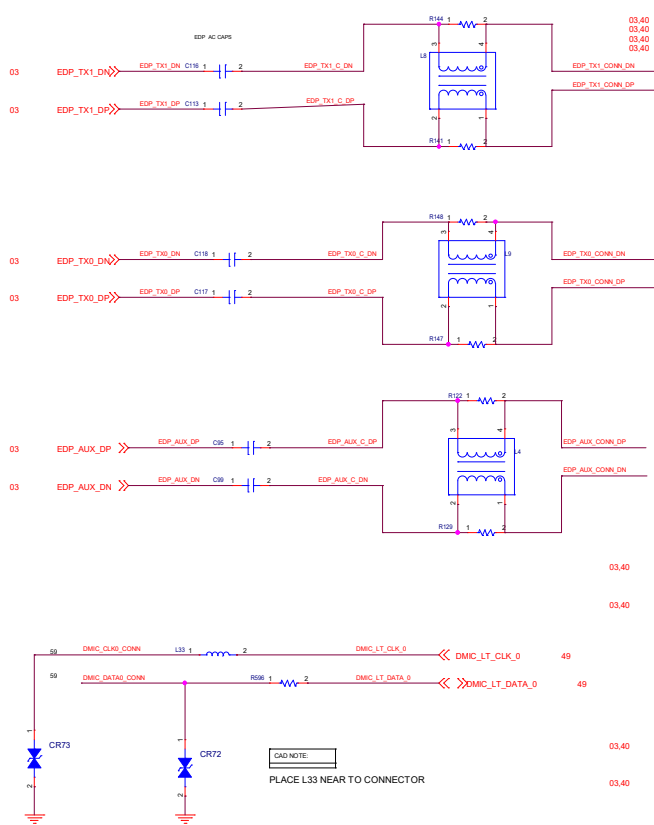
CURRENT LIMIT : 0.523A


## POWER BUTTON



ECO No.		 代通电子	
Designer			
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Approved		Title:	
DO NOT REPRODUCE		Size Custom	Schematic
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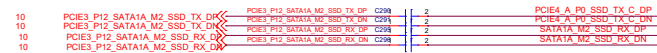
## EDP AND CAM CONNECTOR




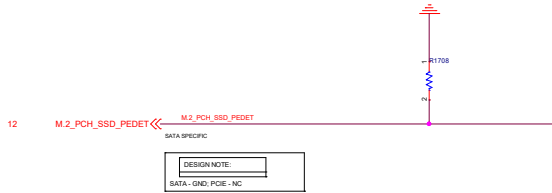
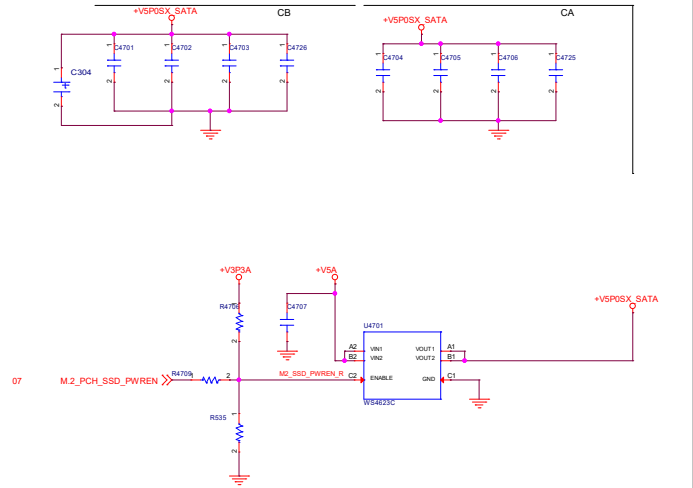
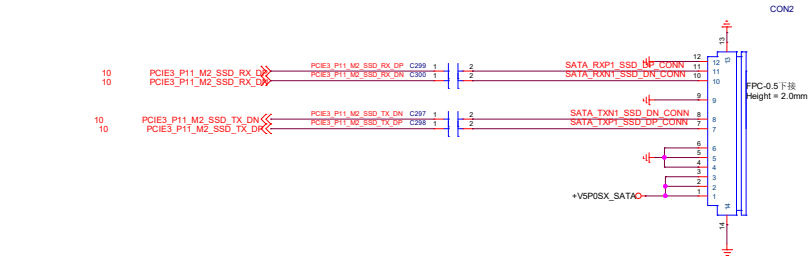
ECO No.			
Designer			
Reviewed			
Approved			
TITLE:		Size: SCHEMATIC Custom:	
DO NOT REPRODUCE		Drawing No.	
FOR INTERNAL USE ONLY		Date: Tuesday, February 07, 2023	
		Sheet 40 of 69	
		Rev. V1.0	



unmarked as  $\omega_0$  75% from  $\text{Deviation}(\omega_0) = 4.0$  to 4.0



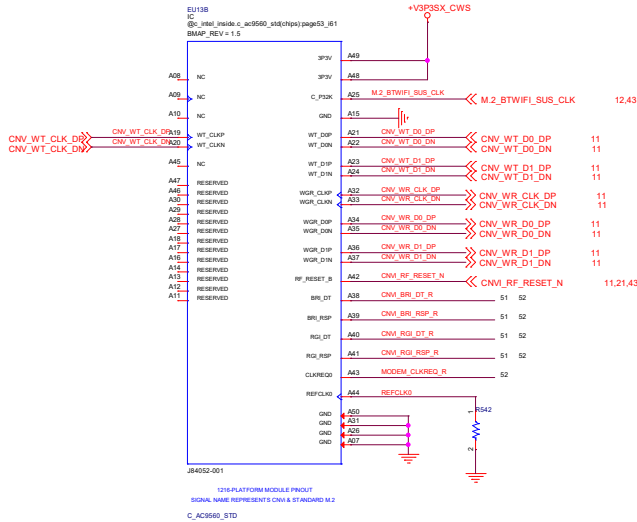
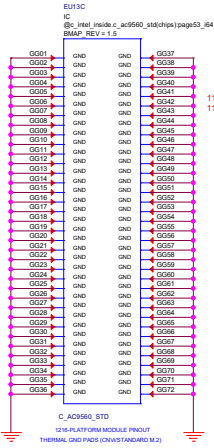
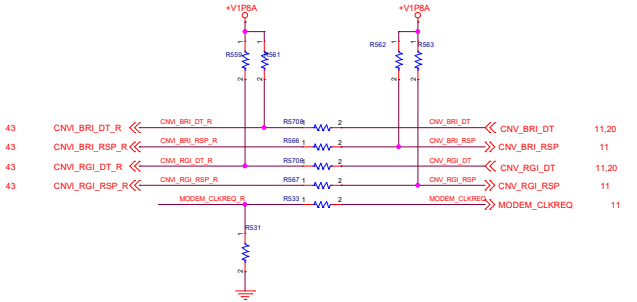
ECO No.		 亿道电子
Designer		
Reviewed		
Approved		
		<b>Title:</b>
DO NOT REPRODUCE	Size A3	SCHEMATIC
FOR INTERNAL USE ONLY	Date: Tuesday, February 07, 2023	Drawing No. Sheet 41 of 69
		Rev. V1.0



ECO No.		Fidwin 机通电子	
Designer			
Reviewed			
Approved		Title:	
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No. Rev.
cre. systems corp. corp.	JHU	Tuesday, February 07, 2023	Sheet 42 of 68




WLAN\_CNVI

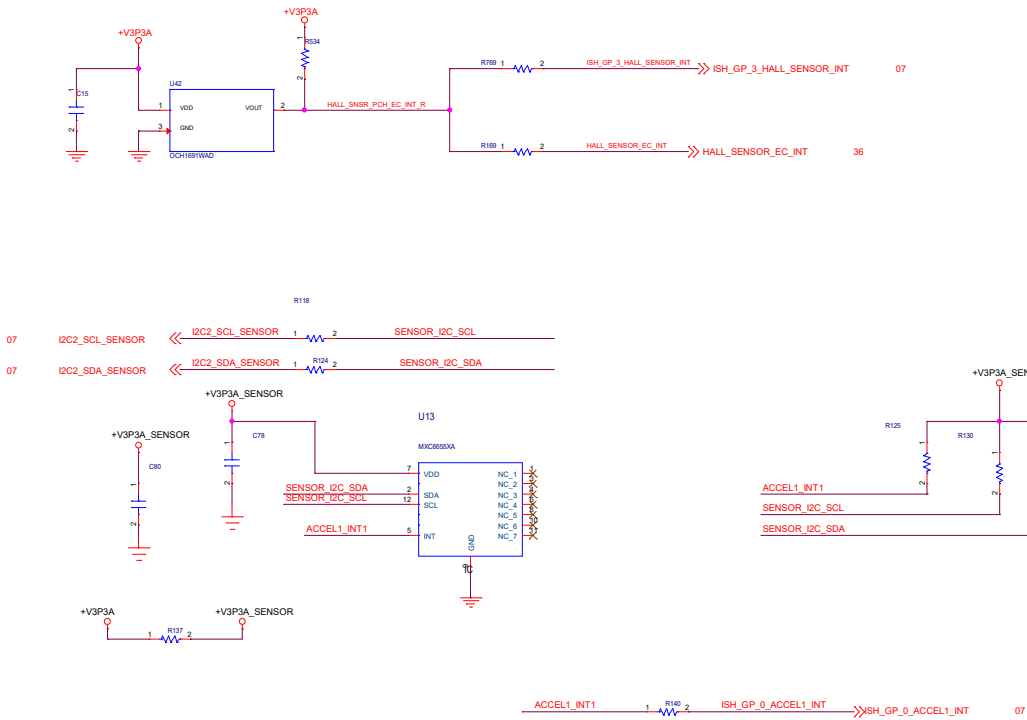


ECO No.		Fidwin 机通电子		
Designer				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No.	Rev.
cnv_systems.cnv.cn	1402	Tuesday, February 07, 2023	Sheet 44	of 68



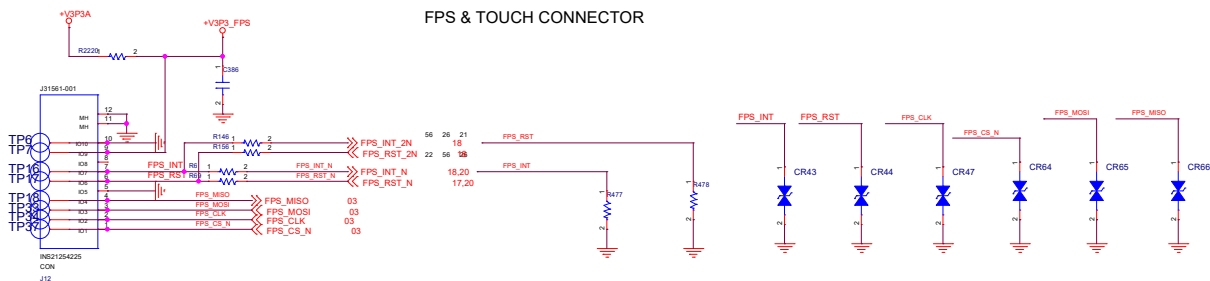
ECO No.				
Designed				
Drawn				
Approved		Title:		
DO NOT REPRODUCE		Size	SCHEMATIC	Drawing No.
FOR INTERNAL USE ONLY		Unit	Tuesday, February 07, 2023	Rev. V1.0
		Sheet	45	of 59

HALL SENSOR



ECO No.			
Designer		Title:	
Reviewed		81ms	SCHEMATIC
Approved		B	Rev. V1.0
DO NOT REPRODUCE		Date: Tuesday February 07, 2023	Sheet 46 of 69
FOR INTERNAL USE ONLY			

## FPS & TOUCH CONNECTOR



V3P3A

50Ω

2

V3P3\_TCH

1k

1k

1k

1k

TCHPAD\_DCO\_SCL

TCHPAD\_DCO\_SDA

TCHPAD\_INT\_N

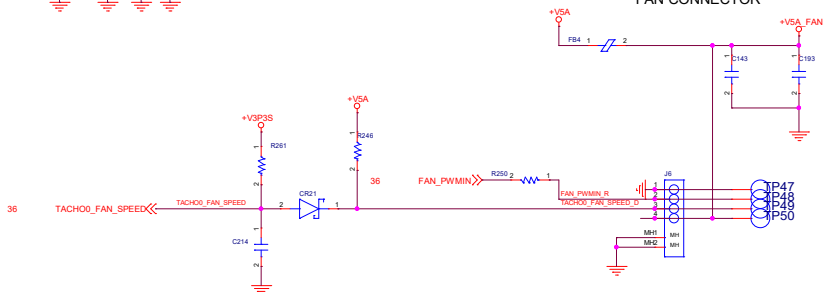
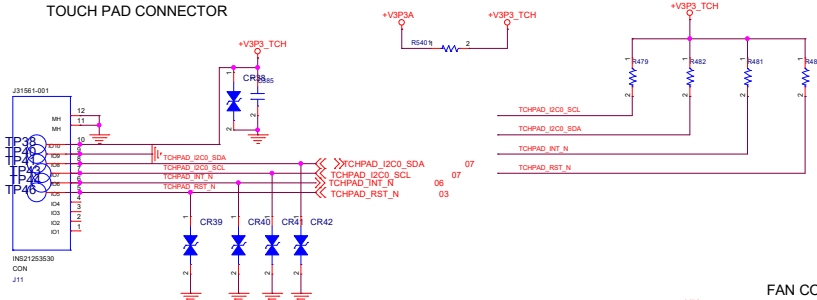
TCHPAD\_RST\_N

D\_DCO\_SDA 07

CQO\_SCL 06

RT\_N 03


RST\_N




FAN NUT



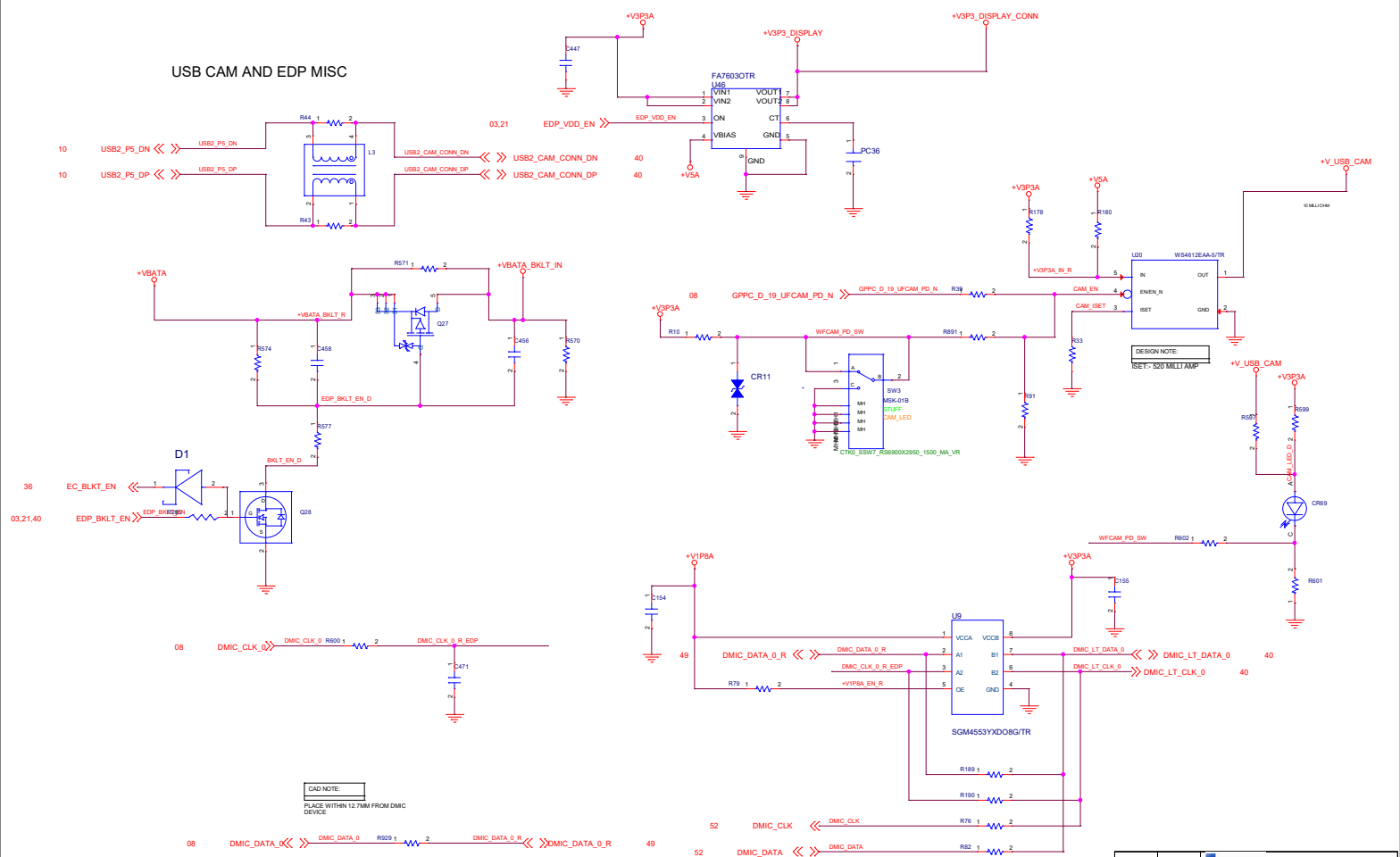
BPAGE DRAWING


ECO No.		 代通电子		
Designer				
Reviewed				
Approved		<b>Title:</b> Size A3 SCHEMATIC Drawing No. Rev. V1		
DO NOT REPRODUCE		Date: Tuesday, February 07, 2023	Sheet 47 of 69	
FOR INTERNAL USE ONLY				

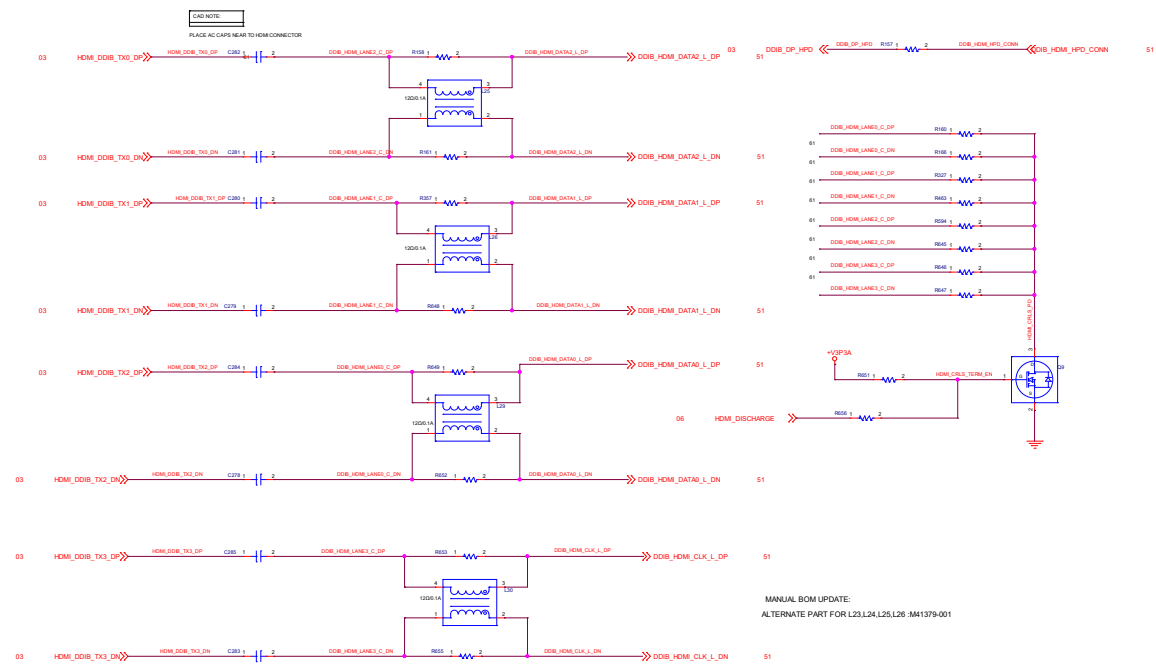
ECO No.			
Designer			
Reviewed			
Approved		Title:	
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No. V1.0
rev. 000000.000.000	1/10/2020	Tuesday, December 13, 2020	Sheet 48 of 68



## USB CAM AND EDP MISC

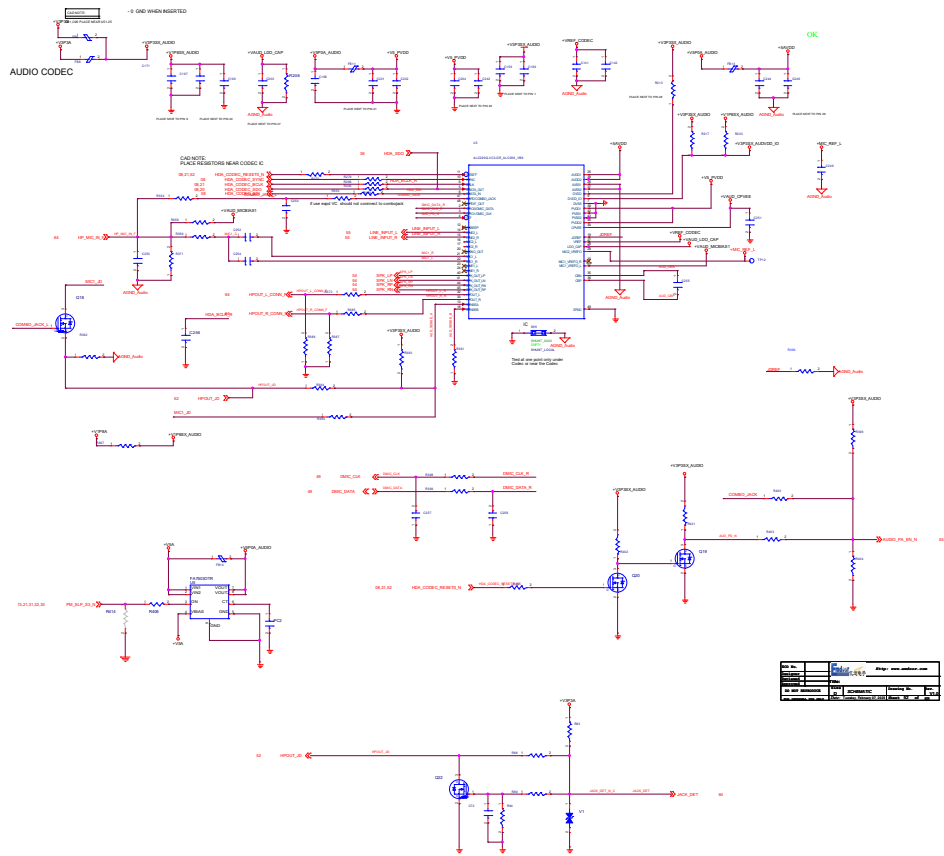



ECO No.				 代理电子
Designer				
Reviewed				
Approved				
Title:				
DO NOT REPRODUCE	Size A3	SCHEMATIC	Drawing No.	Rev. V1.0
FOR INTERNAL USE ONLY	Date: Tuesday, February 07, 2023		Sheet 49 of 68	

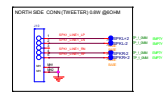


ECO No.		 宏捷电子		
Designer				
Reviewed				
Approved			Title:	
DO NOT REPRODUCE	Size C	SCHEMATIC	Drawing No.	Rev. V1.0
FOR INTERNAL USE ONLY	Date: Tuesday, February 07, 2012	Sheet 50 of 59		





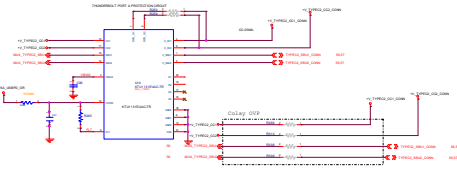
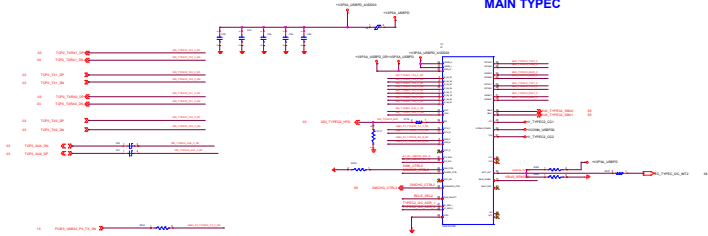
ECO No.				
Designed				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No.	Rev.
ENDWAY ELECTRONICS CO., LTD.	DATE: Tuesday, December 13, 2023	Sheet 53	of 68	V1.0



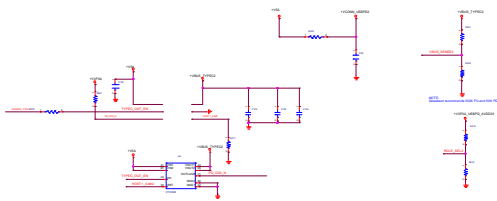
REG No.		 <a href="http://www.fujian.com">http://www.fujian.com</a>			
Product No.					
Manufacturer					
Model No.					
REG NO. INFORMATION		REG No.	REG No.	REG No.	REG No.
REG No. INFORMATION		REG No.	REG No.	REG No.	REG No.



# MAIN TYPEC

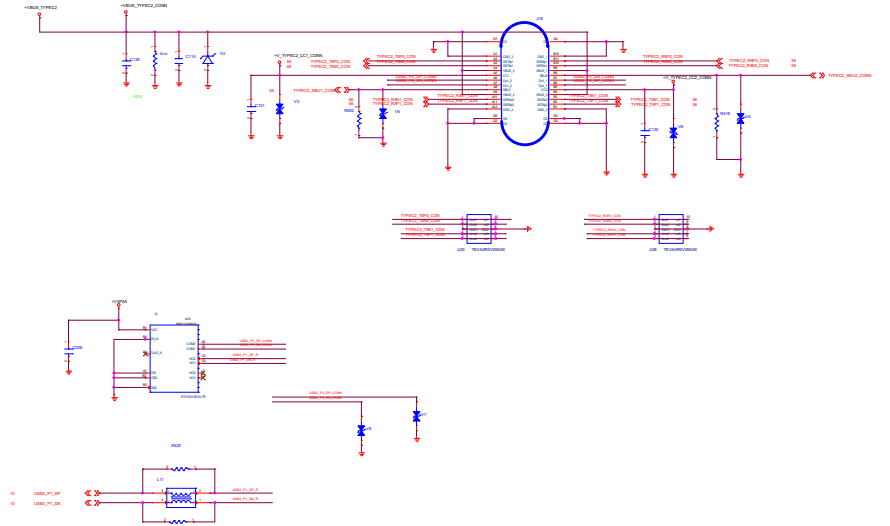


Symbol	Value	Unit
R1	10k	Ω
R2	10k	Ω
R3	10k	Ω
R4	10k	Ω
R5	10k	Ω
R6	10k	Ω
R7	10k	Ω
R8	10k	Ω
R9	10k	Ω
R10	10k	Ω
R11	10k	Ω
R12	10k	Ω
R13	10k	Ω
R14	10k	Ω
R15	10k	Ω
R16	10k	Ω
R17	10k	Ω
R18	10k	Ω
R19	10k	Ω
R20	10k	Ω
R21	10k	Ω
R22	10k	Ω
R23	10k	Ω
R24	10k	Ω
R25	10k	Ω
R26	10k	Ω
R27	10k	Ω
R28	10k	Ω
R29	10k	Ω
R30	10k	Ω
R31	10k	Ω
R32	10k	Ω
R33	10k	Ω
R34	10k	Ω
R35	10k	Ω
R36	10k	Ω
R37	10k	Ω
R38	10k	Ω
R39	10k	Ω
R40	10k	Ω
R41	10k	Ω
R42	10k	Ω
R43	10k	Ω
R44	10k	Ω
R45	10k	Ω
R46	10k	Ω
R47	10k	Ω
R48	10k	Ω
R49	10k	Ω
R50	10k	Ω
R51	10k	Ω
R52	10k	Ω
R53	10k	Ω
R54	10k	Ω
R55	10k	Ω
R56	10k	Ω
R57	10k	Ω
R58	10k	Ω
R59	10k	Ω
R60	10k	Ω
R61	10k	Ω
R62	10k	Ω
R63	10k	Ω
R64	10k	Ω
R65	10k	Ω
R66	10k	Ω
R67	10k	Ω
R68	10k	Ω
R69	10k	Ω
R70	10k	Ω
R71	10k	Ω
R72	10k	Ω
R73	10k	Ω
R74	10k	Ω
R75	10k	Ω
R76	10k	Ω
R77	10k	Ω
R78	10k	Ω
R79	10k	Ω
R80	10k	Ω
R81	10k	Ω
R82	10k	Ω
R83	10k	Ω
R84	10k	Ω
R85	10k	Ω
R86	10k	Ω
R87	10k	Ω
R88	10k	Ω
R89	10k	Ω
R90	10k	Ω
R91	10k	Ω
R92	10k	Ω
R93	10k	Ω
R94	10k	Ω
R95	10k	Ω
R96	10k	Ω
R97	10k	Ω
R98	10k	Ω
R99	10k	Ω
R100	10k	Ω

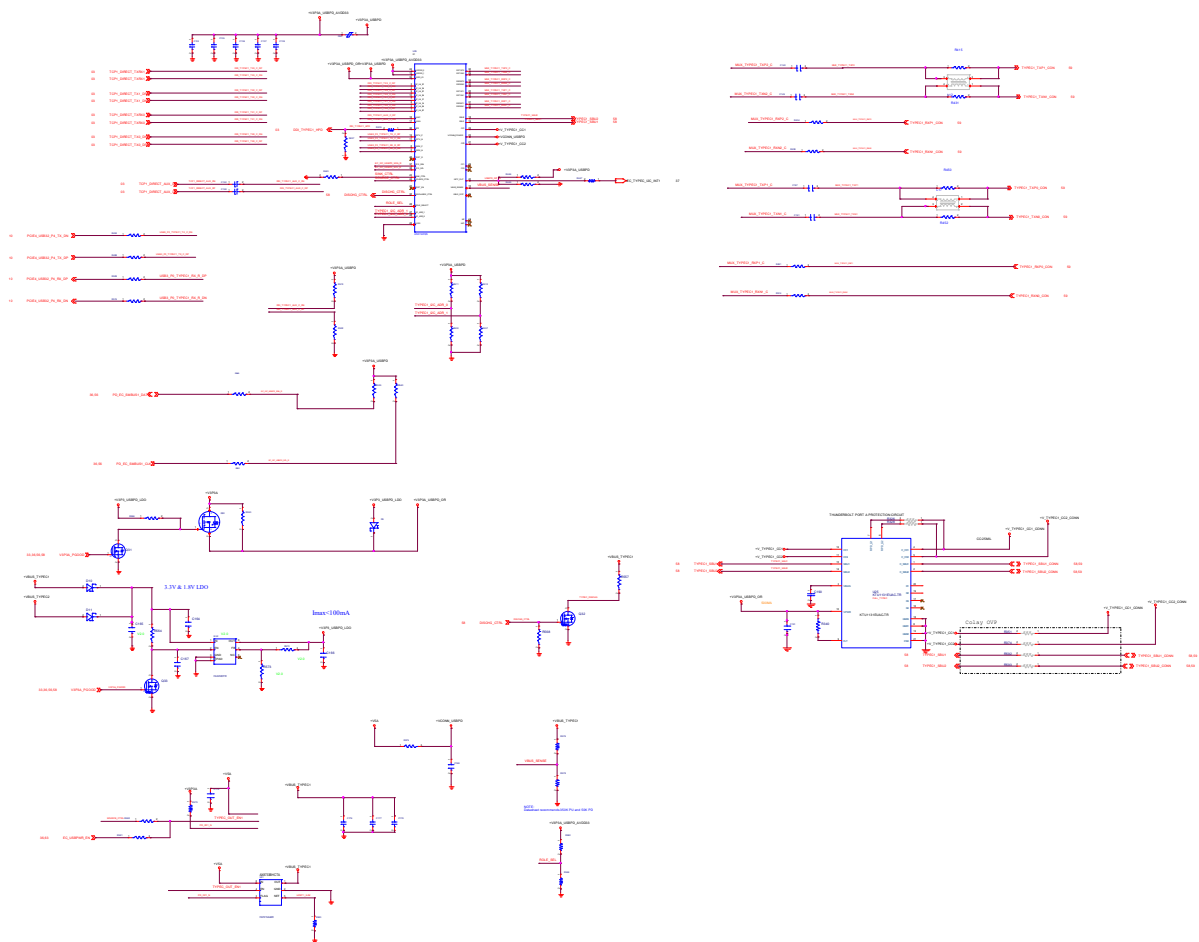




# MAIN TYPEC

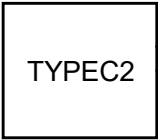


Rev	1	Rev	1
Date	2023-01-10	Date	2023-01-10
By	John Doe	By	John Doe
Check	John Doe	Check	John Doe
Approved	John Doe	Approved	John Doe





MAIN  
typec



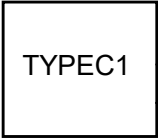
I2C Address:0x56 0x6e  
0x78 0x86

INT2

GPJ1

I2C  
SMBUS0

second typec for  
other





I2C Address:0x58 0x7e  
0x7a 0x84

INT1

GPC3

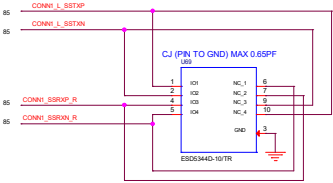
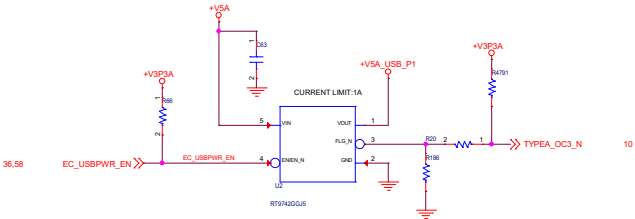
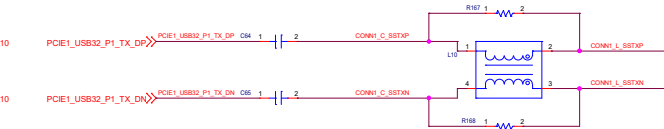
DWG No.		 机通电子	
Designer			
Reviewed		Title:	
Approved			
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No. V1.0
cre. systems corp. corp.	JHU	Tuesday, December 13, 2023	Sheet 60 of 68

ECO No.				
Designed				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No.	Rev.
ENDWAY ELECTRONICS CORP. CHINA	DATE: Tuesday, December 13, 2023	Sheet 51	of 68	V1.0

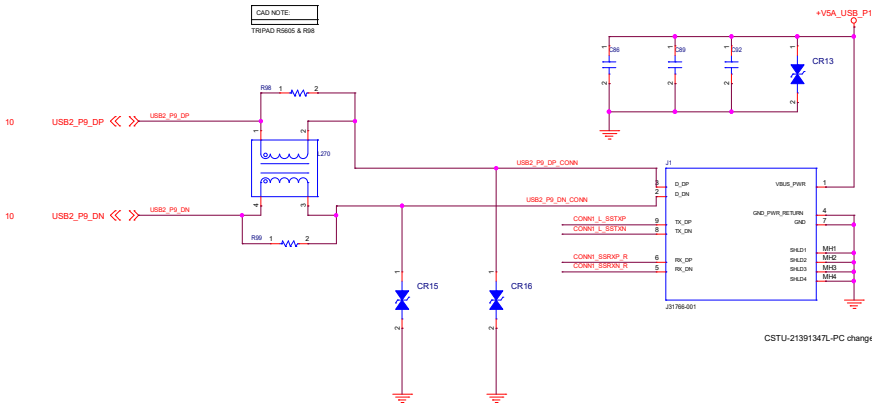
ECO No.				
Designed				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No.	Rev.
ENDWAY ELECTRONICS CO., LTD.	DATE: Tuesday, December 13, 2023	Sheet 62	of 68	V1.0

USB3 TYPE-A #1(RIGHT CONN)

MANUAL BOM UPDATE:  
ALTERNATE PART FOR L10.M41379-001



CAD NOTE:  
TRAPAD R885 & R88




CSTU-21391347L-PC change insertion and extraction force

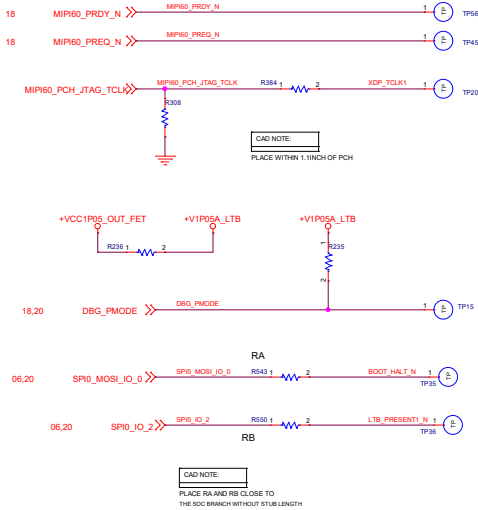
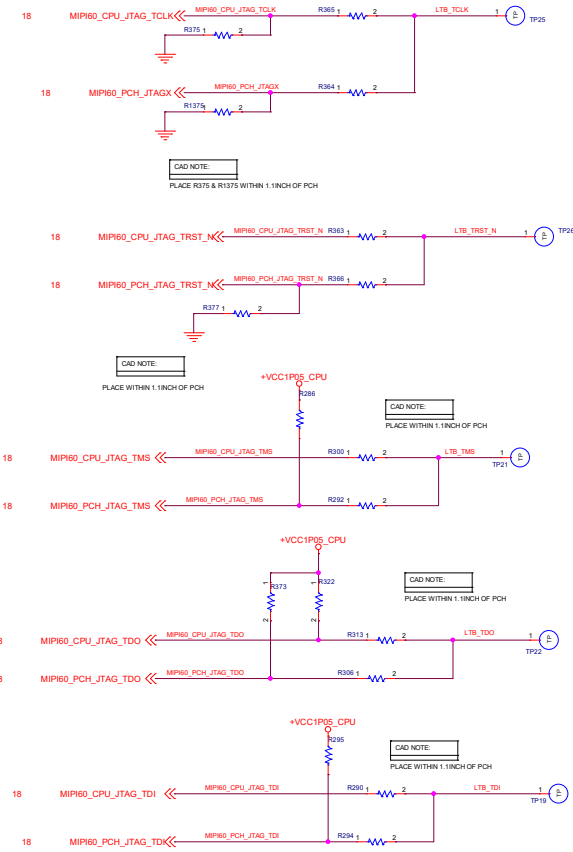
DOC No.		Fidwin 飞威电子
Design		
Drawn		
Approved		
DO NOT REPRODUCE	8104	SCHEMATIC
For systems use only	Created: Tuesday, February 07, 2012	Sheet 53 of 69





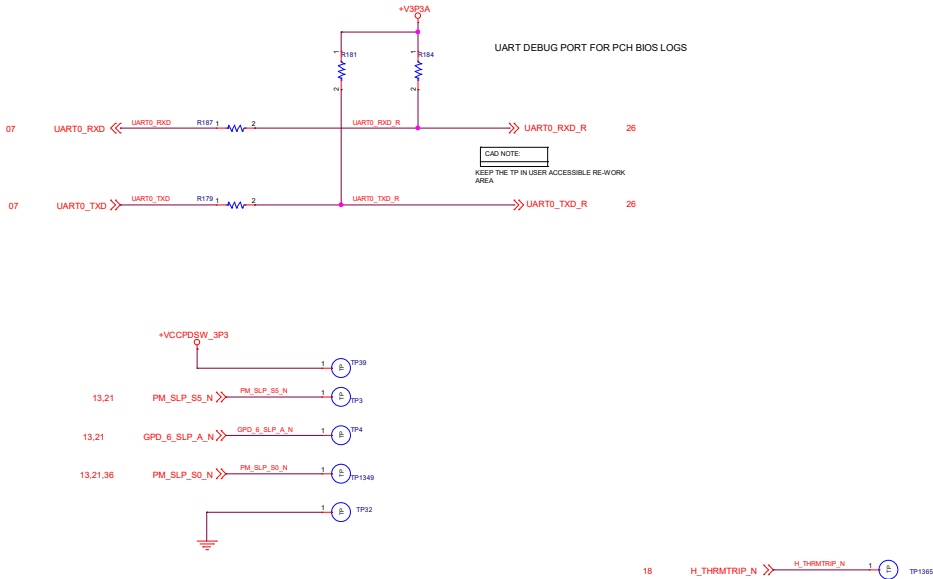
ECO No.			
Designer			
Reviewed			
Approved		Title:	
DO NOT REPRODUCE	Size	SCHMATIC	Drawing No.
FOR INTERNAL USE ONLY	A3	Tue, 13 Dec 2022	Rev. V1.0
		Sheet 85	of 89

DEBUG TEST POINTS -1




ECO No.		Fidwin 机通电子	
Designer		Title:	
Reviewed		S12a	
Approved		A3 SCHEMATIC Drawing No. Rev.	
DO NOT REPRODUCE		V1.0	
eco_systems_esp_china	JHU	Tuesday, February 07, 2023	Sheet 66 of 69

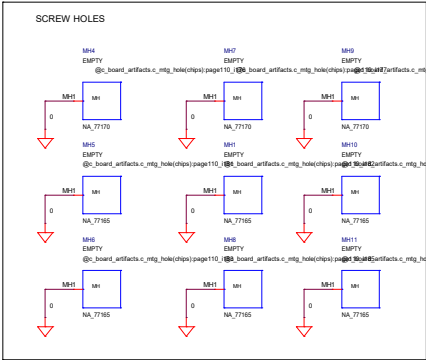
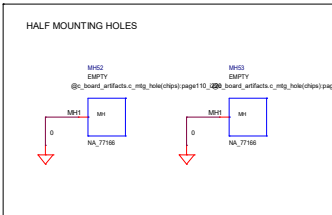
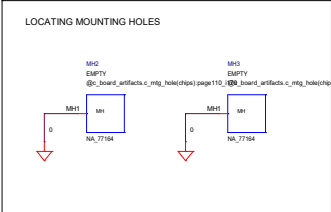
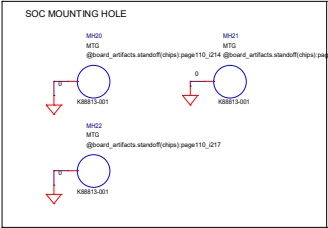
DEBUG TEST POINTS -2



ECO No.		Fidwin 机通电子		
Designer				
Reviewed		Title:		
Approved		Size	Drawing No.	Rev.
DO NOT REPRODUCE	A3	SCHEMATIC		V1.0
eco_systems.com.cn	JHU	Tuesday, February 07, 2023	Sheet 57	of 68

ECO No.			
Designed			
Reviewed			
Approved		Title:	
DO NOT REPRODUCE	A3	SCHEMATIC	Drawing No. V1.0
File: 20230905_001.dwg	File: Tuesday, December 13, 2023	Sheet 68	of 68

MOUNTING HOLES



ECO No.			
Designer			
Reviewed			
Approved			
By: WIP: 0000000000	File: SCHEMATIC	Drawing No.	Rev.
WIP: 0000000000	Client: Custom	Sheet: 08 of 09	V1.0